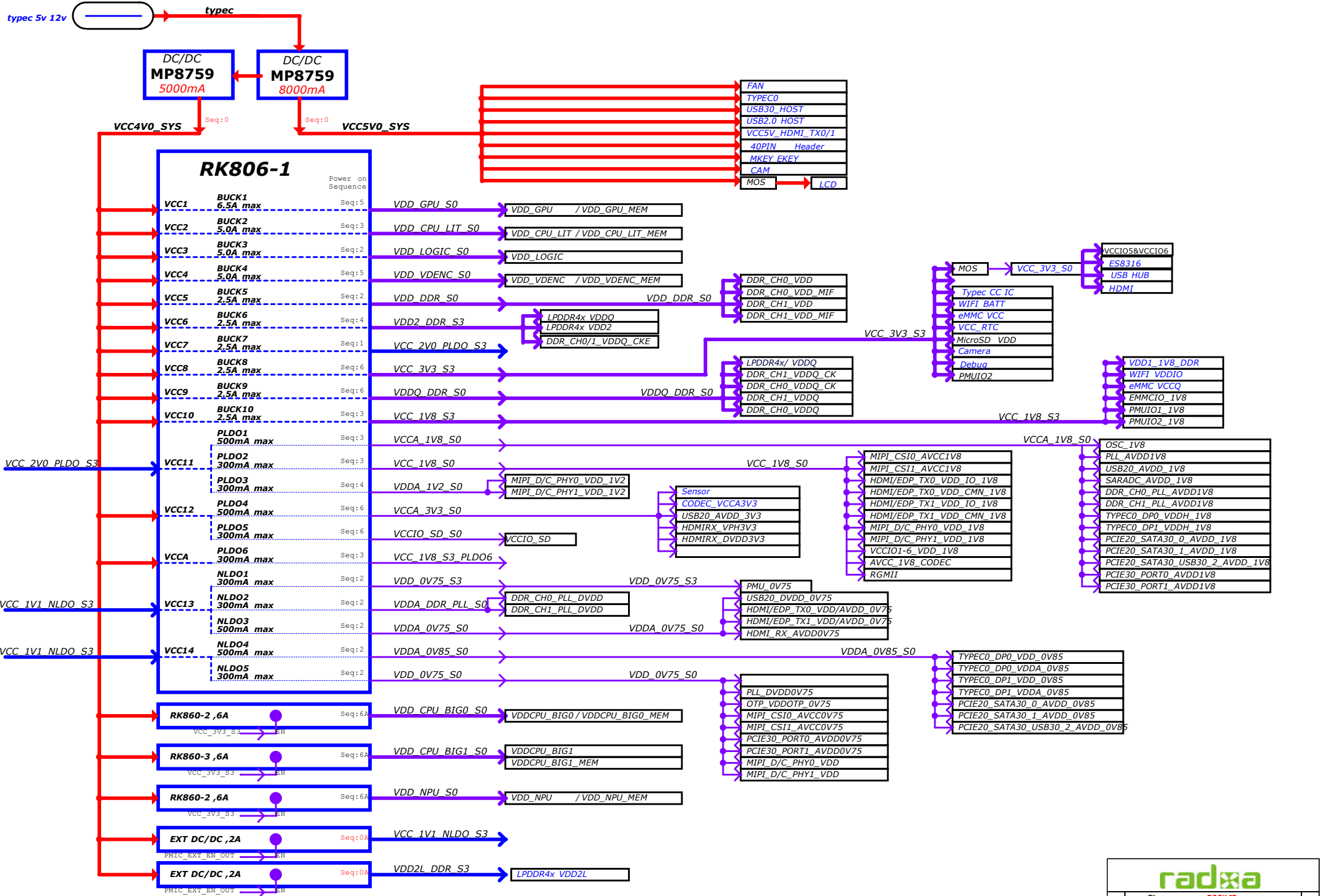


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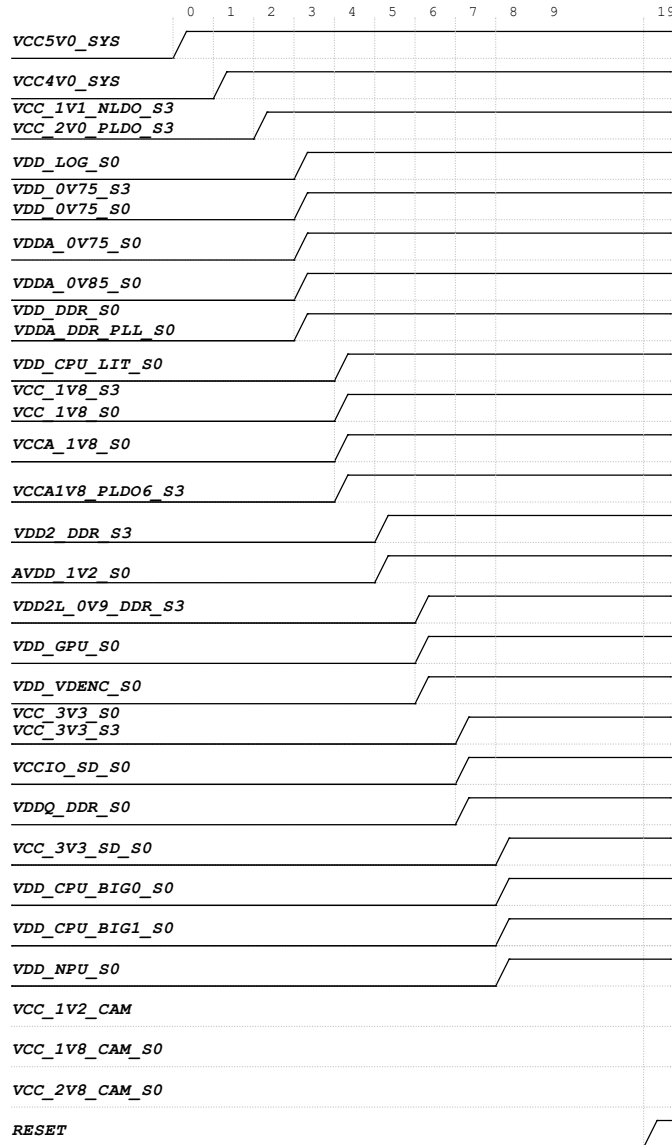
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Power Tree



Power Sequence



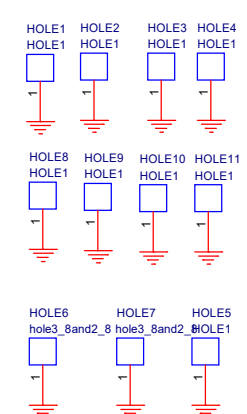
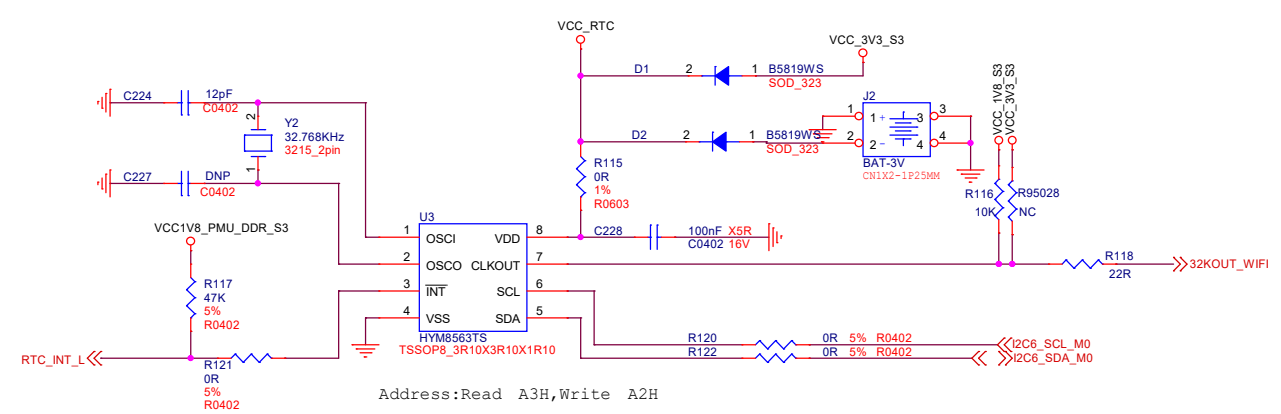
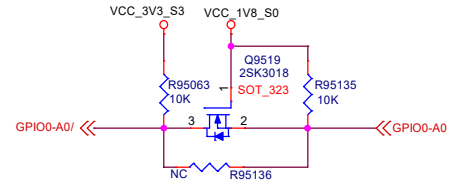
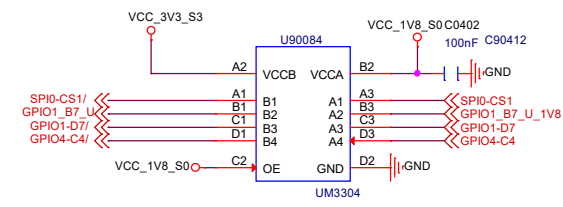
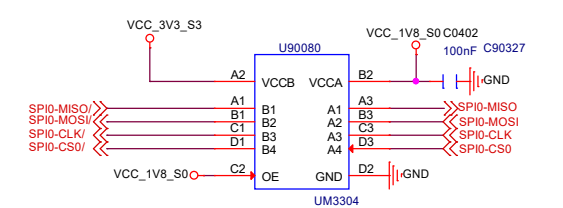
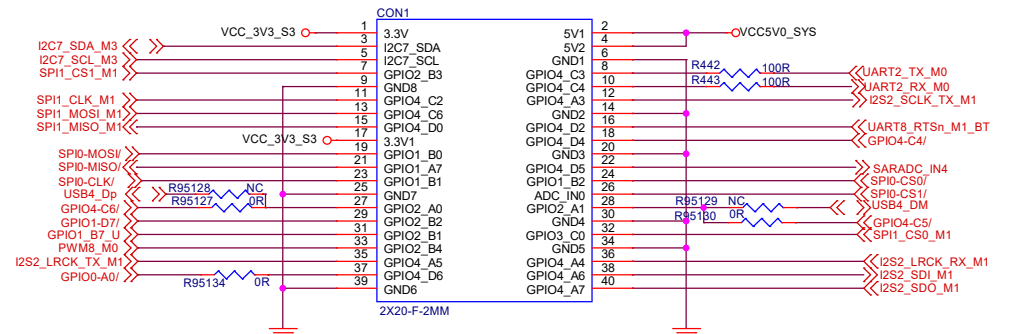
Power Supply	PMIC Channel	Supply Limit	Power Name	Time Slot	Default Voltage	Default ON/OFF	Sleep ON/OFF
VCC4V0_SYS	RK806-1_BUCK1	6.5A	VDD_GPU_S0	Slot:5	0.75V	ON	OFF
VCC4V0_SYS	RK806-1_BUCK2	5A	VDD_CPU_LIT_S0	Slot:3	0.75V	ON	OFF
VCC4V0_SYS	RK806-1_BUCK3	5A	VDD_LOG_S0	Slot:2	0.75V	ON	OFF
VCC4V0_SYS	RK806-1_BUCK4	3A	VDD_VDENC_S0	Slot:5	0.75V	ON	OFF
VCC4V0_SYS	RK806-1_BUCK5	2.5A	VDD_DDR_S0	Slot:2	0.85V	ON	OFF
VCC4V0_SYS	RK806-1_BUCK6	2.5A	VDD2_DDR_S3	Slot:4	ADJ FB=0.5V	ON	ON
VCC4V0_SYS	RK806-1_BUCK7	2.5A	VCC_2V0_PLDO_S3	Slot:1	2.0V	ON	ON
VCC4V0_SYS	RK806-1_BUCK8	2.5A	VCC_3V3_S3	Slot:6	3.3V	ON	ON
VCC4V0_SYS	RK806-1_BUCK9	2.5A	VDDQ_DDR_S0	Slot:6	ADJ FB=0.5V	ON	OFF
VCC4V0_SYS	RK806-1_BUCK10	2.5A	VCC_1V8_S3	Slot:3	1.8V	ON	ON
VCC_2V0_PLDO	RK806-1_PLDO1	0.5A	VCCA_1V8_S0	Slot:3	1.8V	ON	OFF
VCC_2V0_PLDO	RK806-1_PLDO2	0.3A	VCC_1V8_S0	Slot:3	1.8V	ON	OFF
VCC_2V0_PLDO	RK806-1_PLDO3	0.3A	VDDA_1V2_S0	Slot:4	1.2V	ON	OFF
VCC4V0_SYS	RK806-1_PLDO4	0.5A	VCCA_3V3_S0	Slot:6	3.3V	ON	OFF
VCC4V0_SYS	RK806-1_PLDO5	0.3A	VCCIO_SD_S0	Slot:6	3.3V	ON	OFF
VCC4V0_SYS	RK806-1_PLDO6	0.3A	VCCA1V8_PLDO6_S3	Slot:3	1.8V	ON	ON
VCC_1V1_NLDO	RK806-1_NLDO1	0.3A	VDD_0V75_S3	Slot:2	0.75V	ON	ON
VCC_1V1_NLDO	RK806-1_NLDO2	0.3A	VDDA_DDR_PLL_S0	Slot:2	0.85V	ON	OFF
VCC_1V1_NLDO	RK806-1_NLDO3	0.5A	VDDA_0V75_S0	Slot:2	0.75V	ON	OFF
VCC_1V1_NLDO	RK806-1_NLDO4	0.5A	VDDA_0V85_S0	Slot:2	0.85V	ON	OFF
VCC_1V1_NLDO	RK806-1_NLDO5	0.3A	VDD_0V75_S0	Slot:2	0.75V	ON	OFF
VCC4V0_SYS	BUCK_RK860-2	6A	VDD_CPU_BIG0_S0	Slot:6A	0.75V	ON	OFF
VCC4V0_SYS	BUCK_RK860-3	6A	VDD_CPU_BIG1_S0	Slot:6A	0.75V	ON	OFF
VCC4V0_SYS	BUCK_RK860-2	6A	VDD_NPU_S0	Slot:6A	0.75V	ON	OFF
VCC4V0_SYS	EXT_BUCK	2A	VCC_1V1_NLDO_S3	Slot:1	1.1V	ON	ON
VCC4V0_SYS	EXT_BUCK	2A	VDD2L_0V9_DDR_S3	Slot:5	0.9V	ON	ON
VCC4V0_SYS	EXT_BUCK	2.5A	VCC_3V3_SD_S0	Slot:6A	3.3V	ON	OFF
VCC_3V3_S3	EXT_BUCK	2A	VCC_1V2_CAM_S0	OFF	1.2V	OFF	OFF
VCC_3V3_S3	LDO_PT5108	0.5A	VCC_1V8_CAM_S0	OFF	1.8V	OFF	OFF
VCC_3V3_S3	LDO_PT5108	0.5A	VCC_2V8_CAM_S0	OFF	2.8V	OFF	OFF

IO Power Domain Map

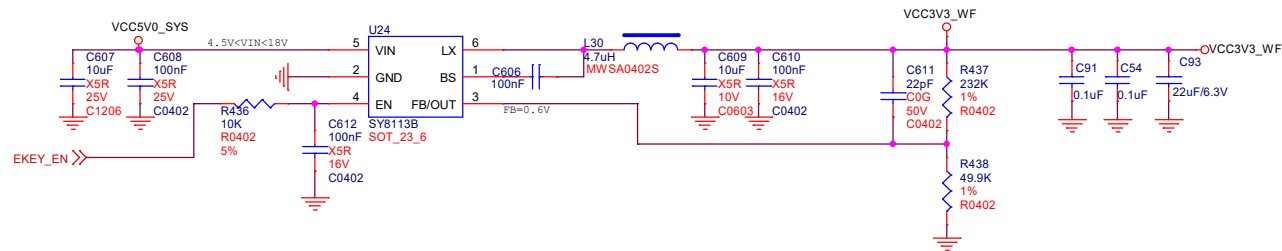
IO Domain	Pin Num	Support IO Voltage	Supply Power Pin Name	Power Source	IO Operating Voltage
PMUIO1	Pin N28	1.8V Only	PMUIO1_1V8	VCC_1V8_S3	1.8V
PMUIO2	Pin R27 Pin P28	1.8V or 3.3V	PMUIO2_1V8 PMUIO2	VCC_1V8_S3	1.8V
EMMCIO	Pin V26	1.8V Only	EMMCIO_1V8	VCC_1V8_S0	1.8V
VCCIO1	Pin G20	1.8V Only	VCCIO1_1V8	VCC_1V8_S0	1.8V
VCCIO2	Pin AA7 Pin Y7	1.8V or 3.3V	VCCIO2_1V8 VCCIO2	VCC_1V8_S0 VCCIO_SD_S0	1.8V 1.8V/3.3V
VCCIO3	Pin Y26	1.8V Only	VCCIO3_1V8	VCC_1V8_S0	1.8V
VCCIO4	Pin H20 Pin H21	1.8V or 3.3V	VCCIO4_1V8 VCCIO4	VCC_1V8_S0 VCC_1V8_S0	1.8V
VCCIO5	Pin W25 Pin W26	1.8V or 3.3V	VCCIO5_1V8 VCCIO5	VCC_1V8_S0 VCC_3V3_S0	1.8V 3.3V
VCCIO6	Pin AC25 Pin AC26	1.8V or 3.3V	VCCIO6_1V8 VCCIO6	VCC_1V8_S0 VCC_3V3_S0	1.8V 3.3V



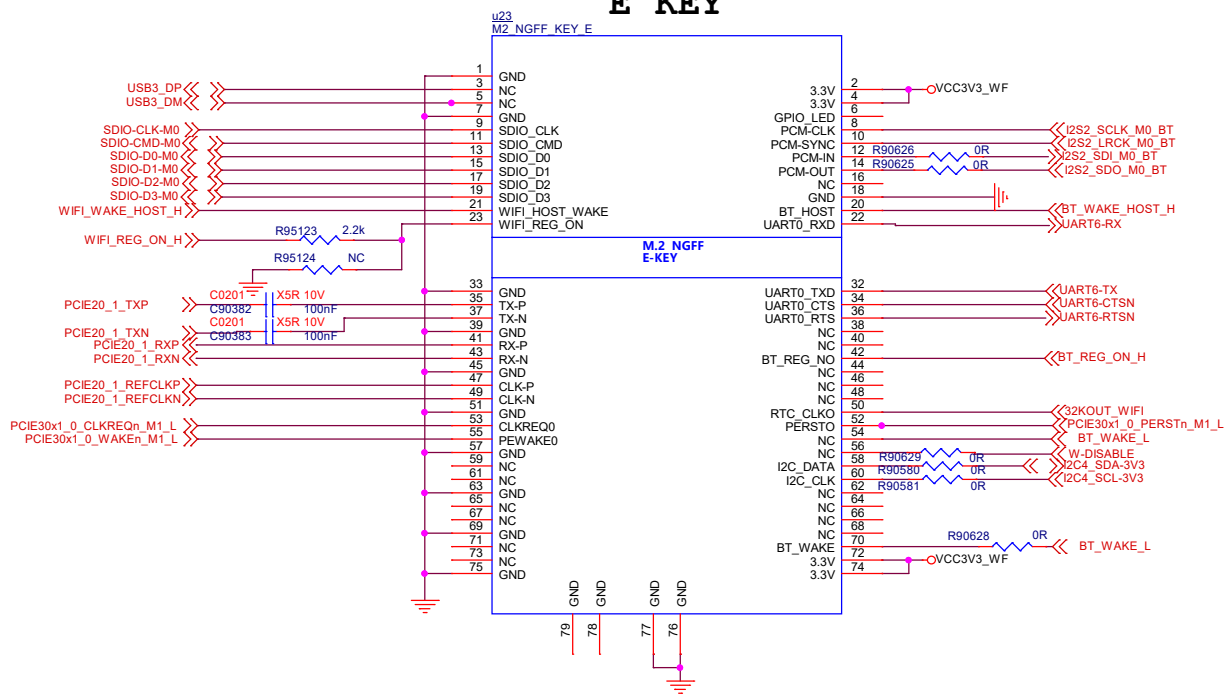
UART TO USB (DEBUG)



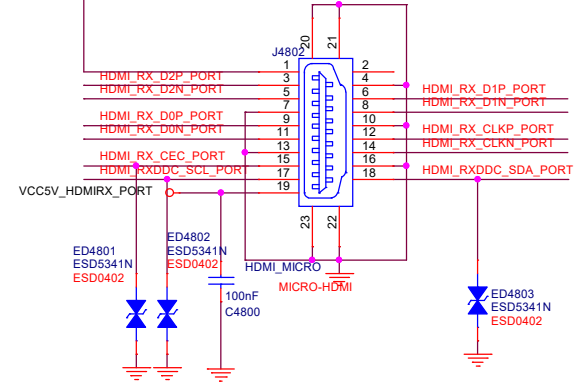
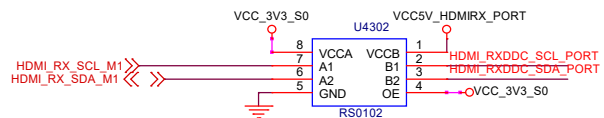
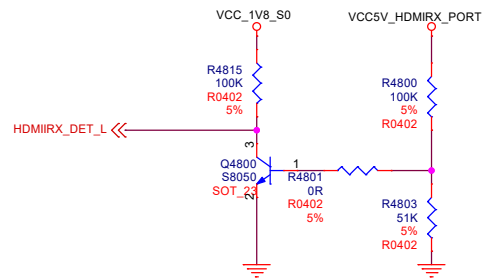
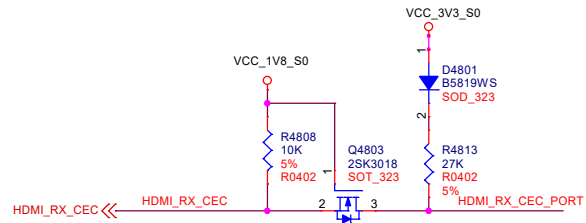
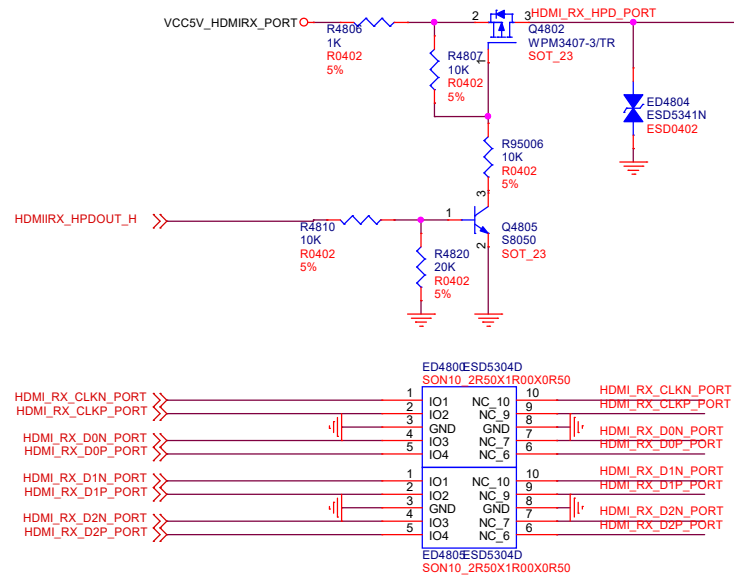
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Date:	Page Name:	Sheet
Tuesday, September 20, 2022	05.CONNECT	5 of 32



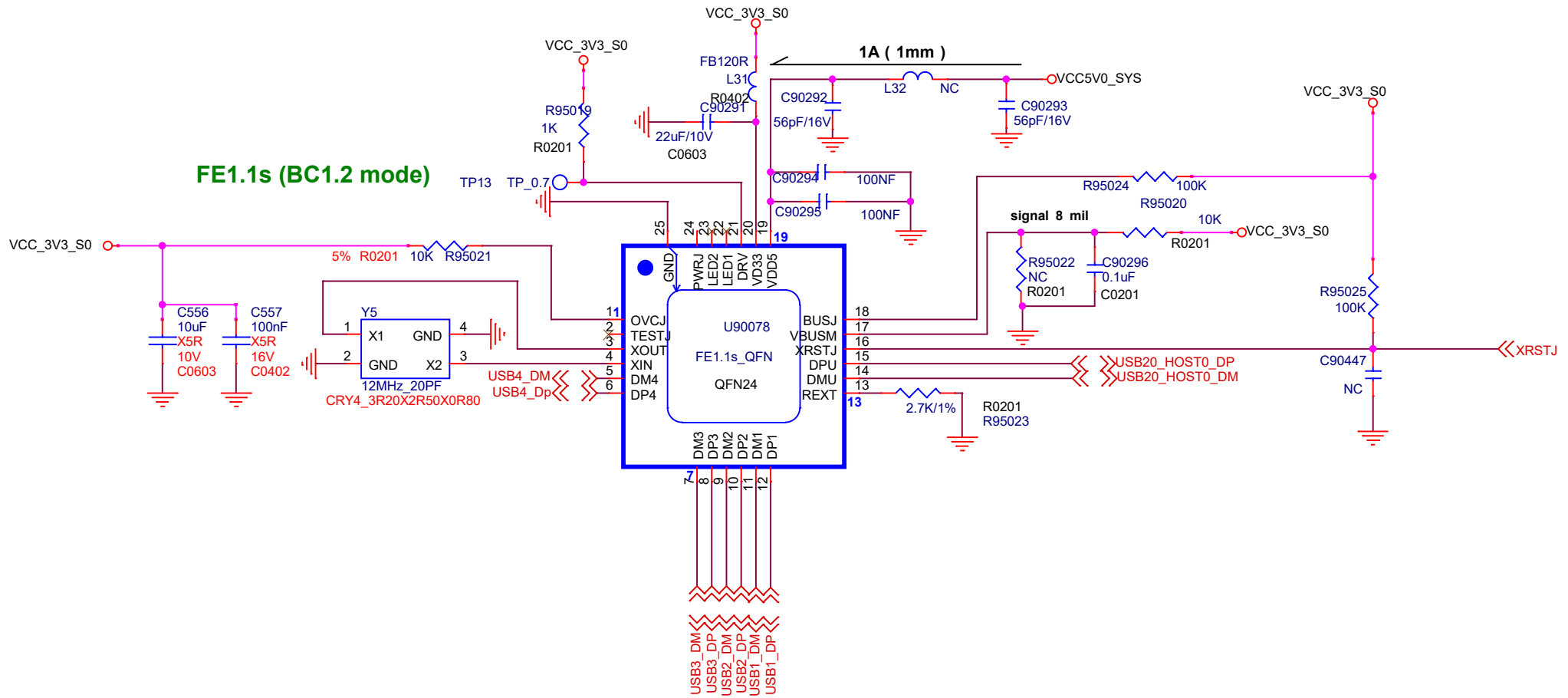
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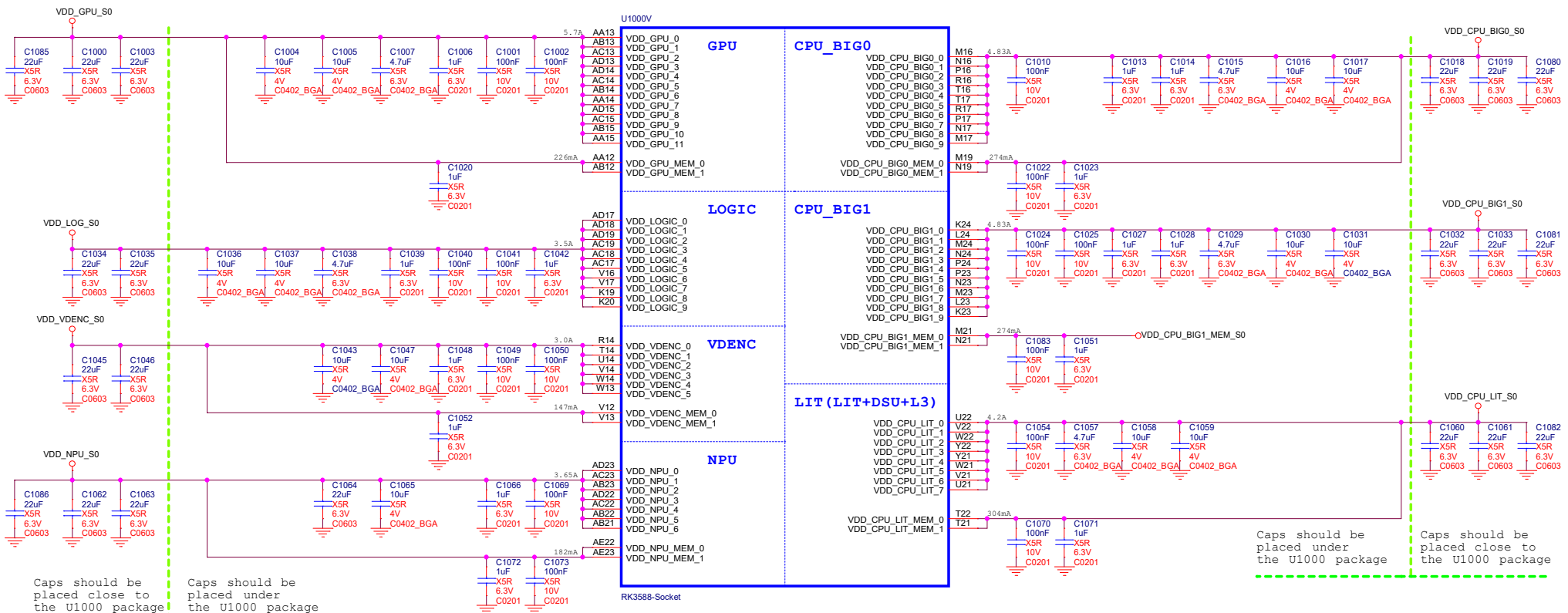


HDMI2.0 RX



FE1.1s (BC1.2 mode)





Caps should be placed under the U1000 package

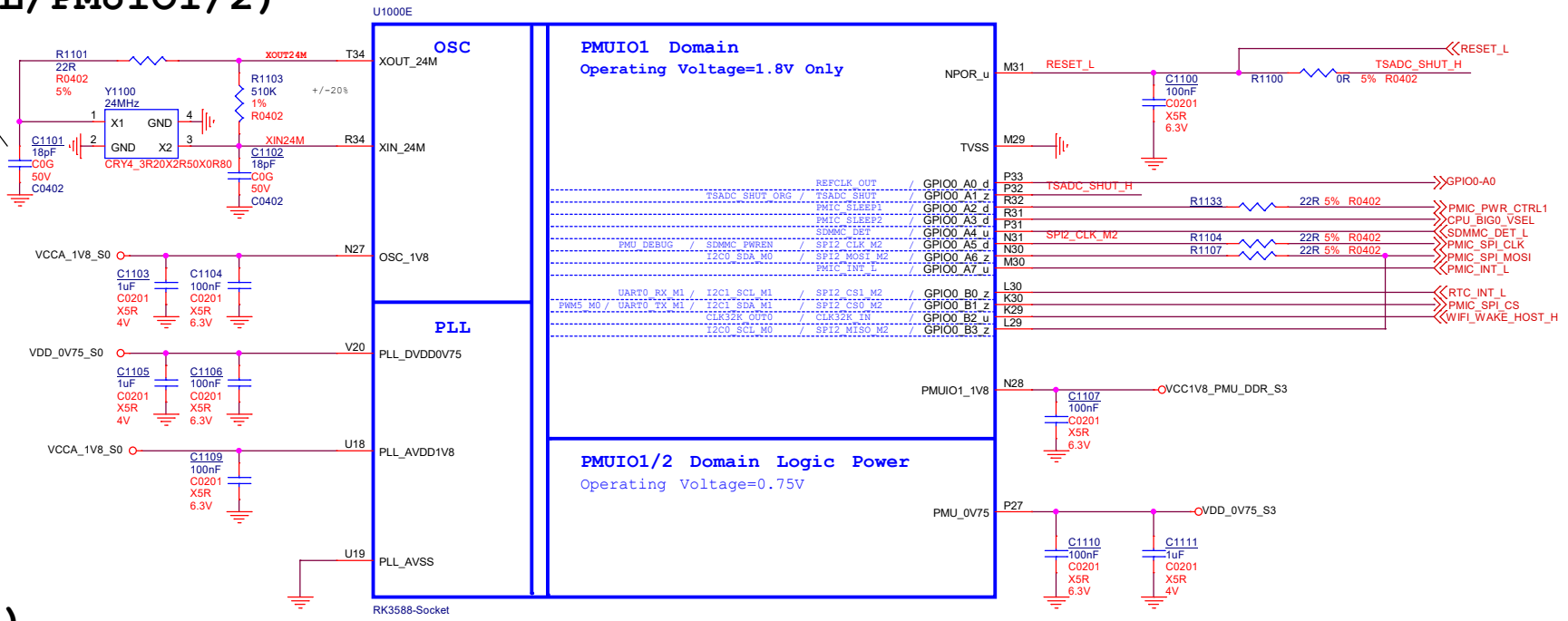
Caps should be placed close to the U1000 package

H28	AVSS_1	AH12	L3	VSS_107	VSS_160	R19	A1	VSS_1	VSS_54	F15	W3	VSS_213	VSS_266	AB18
H31	AVSS_2	AH15	L6	VSS_108	VSS_161	R20	A11	VSS_2	VSS_55	F16	W6	VSS_214	VSS_267	AB19
J27	AVSS_3	AH21	L9	VSS_109	VSS_162	R21	A14	VSS_3	VSS_56	F19	W7	VSS_215	VSS_268	AB20
J28	AVSS_4	AH22	L19	VSS_110	VSS_163	R22	A34	VSS_4	VSS_57	F20	W9	VSS_216	VSS_269	AB21
J29	AVSS_5	AH23	L20	VSS_111	VSS_164	R23	B6	VSS_5	VSS_58	F21	W10	VSS_217	VSS_270	AB22
J32	AVSS_6	AJ3	L21	VSS_112	VSS_165	R24	B19	VSS_6	VSS_59	F22	W11	VSS_218	VSS_271	AB23
K26	AVSS_7	AJ7	L22	VSS_113	VSS_166	R25	B24	VSS_7	VSS_60	F23	W12	VSS_219	VSS_272	AC3
K31	AVSS_8	AJ8	L25	VSS_114	VSS_167	R26	B27	VSS_8	VSS_61	F31	W15	VSS_220	VSS_273	AC4
K32	AVSS_9	AJ9	M3	VSS_115	VSS_168	R28	B33	VSS_9	VSS_62	G6	W16	VSS_221	VSS_274	AC11
L31	AVSS_10	AJ11	M6	VSS_116	VSS_169	R33	C1	VSS_10	VSS_63	G10	W18	VSS_222	VSS_275	AC12
M26	AVSS_11	AJ15	M14	VSS_117	VSS_170	T6	C5	VSS_11	VSS_64	G15	W19	VSS_223	VSS_276	AC16
M32	AVSS_12	AJ16	M15	VSS_118	VSS_171	T9	C6	VSS_12	VSS_65	G19	W20	VSS_224	VSS_277	AC20
N32	AVSS_13	AJ18	M18	VSS_119	VSS_172	T13	C7	VSS_13	VSS_66	G21	W23	VSS_225	VSS_278	AC21
N35	AVSS_14	AJ21	M20	VSS_120	VSS_173	T20	C8	VSS_14	VSS_67	G12	W24	VSS_226	VSS_279	AC22
AA10	AVSS_15	AJ22	M22	VSS_121	VSS_174	T15	C9	VSS_15	VSS_68	G25	W27	VSS_227	VSS_280	AC27
AB6	AVSS_16	AJ23	M25	VSS_122	VSS_175	T18	C10	VSS_16	VSS_69	G32	Y3	VSS_228	VSS_281	AD11
AB7	AVSS_17	AK4	N3	VSS_123	VSS_176	T19	C11	VSS_17	VSS_70	H3	Y5	VSS_229	VSS_282	AD12
AB8	AVSS_18	AK5	N6	VSS_124	VSS_177	T23	C12	VSS_18	VSS_71	H6	Y6	VSS_230	VSS_283	AD16
AB10	AVSS_19	AK7	N9	VSS_125	VSS_178	T23	C13	VSS_19	VSS_72	H10	Y8	VSS_231	VSS_284	AD20
AC5	AVSS_20	AK10	N11	VSS_126	VSS_179	T24	C14	VSS_20	VSS_73	H12	Y9	VSS_232	VSS_285	AD21
AC8	AVSS_21	AK11	N14	VSS_127	VSS_180	T25	C15	VSS_21	VSS_74	H14	Y10	VSS_233	VSS_286	AD24
AC10	AVSS_22	AK12	N15	VSS_128	VSS_181	T26	C17	VSS_22	VSS_75	H19	Y11	VSS_234	VSS_287	AD25
AD5	AVSS_23	AK13	N18	VSS_129	VSS_182	T27	C18	VSS_23	VSS_76	H22	Y12	VSS_235	VSS_288	AD26
AD8	AVSS_24	AJ25	N20	VSS_130	VSS_183	T27	C18	VSS_24	VSS_77	H25	Y13	VSS_236	VSS_289	AD27
AD10	AVSS_25	AK23	N22	VSS_131	VSS_184	U3	C20	VSS_25	VSS_78	H26	Y14	VSS_237	VSS_290	AE11
AE6	AVSS_26	AL3	N25	VSS_132	VSS_185	U12	C21	VSS_26	VSS_79	J3	Y15	VSS_238	VSS_291	AE12
AE7	AVSS_27	AL4	N26	VSS_133	VSS_186	U13	C22	VSS_27	VSS_80	J4	Y16	VSS_239	VSS_292	AE13
AF4	AVSS_28	AL5	N29	VSS_134	VSS_187	U15	C23	VSS_28	VSS_81	J6	Y17	VSS_240	VSS_293	AE15
AF7	AVSS_29	AL11	P1	VSS_135	VSS_188	U16	C26	VSS_29	VSS_82	J8	Y18	VSS_241	VSS_294	AE18
AF8	AVSS_30	AL13	P3	VSS_136	VSS_189	U17	C28	VSS_30	VSS_83	J10	Y19	VSS_242	VSS_295	AE16
AF9	AVSS_31	AL23	P6	VSS_137	VSS_190	U20	C30	VSS_31	VSS_84	J11	Y20	VSS_243	VSS_296	AE18
AF11	AVSS_32	AM4	P8	VSS_138	VSS_191	U23	C32	VSS_32	VSS_85	J12	Y23	VSS_244	VSS_297	AE19
AF12	AVSS_33	AM9	P9	VSS_139	VSS_192	U23	D3	VSS_33	VSS_86	J13	Y24	VSS_245	VSS_298	AE20
AF13	AVSS_34	AM9	P11	VSS_140	VSS_193	U30	D23	VSS_34	VSS_87	J14	Y28	VSS_246	VSS_299	AE21
AF14	AVSS_35	AM18	P14	VSS_141	VSS_194	U31	D24	VSS_35	VSS_88	J15	Y29	VSS_247	VSS_300	AE24
AF15	AVSS_36	AM20	P15	VSS_142	VSS_195	U34	D31	VSS_36	VSS_89	J16	Y30	VSS_248	VSS_301	AF26
AF16	AVSS_37	AM22	P18	VSS_143	VSS_196	U37	E3	VSS_37	VSS_90	J18	Y32	VSS_249	VSS_302	AF27
AF21	AVSS_38	AM23	P19	VSS_144	VSS_197	V4	E6	VSS_38	VSS_91	J19	Y34	VSS_250	VSS_303	AF28
AG3	AVSS_39	AM24	P20	VSS_145	VSS_198	V5	E8	VSS_39	VSS_92	J20	Y35	VSS_251	VSS_304	AF25
AG6	AVSS_40	AM26	P21	VSS_146	VSS_199	V8	E12	VSS_40	VSS_93	J21	Y36	VSS_252	VSS_305	AF27
AG7	AVSS_41	AM28	P22	VSS_147	VSS_200	V9	E18	VSS_41	VSS_94	J22	Y37	VSS_253	VSS_306	AF28
AG10	AVSS_42	AN7	P25	VSS_148	VSS_201	V9	E23	VSS_42	VSS_95	J23	Y38	VSS_254	VSS_307	AF29
AG12	AVSS_43	AN12	P26	VSS_149	VSS_202	V11	E22	VSS_43	VSS_96	J24	Y39	VSS_255	VSS_308	AF30
AG15	AVSS_44	AN23	P34	VSS_150	VSS_203	V15	E23	VSS_44	VSS_97	J25	Y40	VSS_256	VSS_309	AF31
AG18	AVSS_45	AN31	R3	VSS_151	VSS_204	V18	E32	VSS_45	VSS_98	K3	Y41	VSS_257	VSS_310	AF32
AG21	AVSS_46	AP1	R5	VSS_152	VSS_205	V19	F3	VSS_46	VSS_99	K6	Y42	VSS_258	VSS_311	AG30
AG22	AVSS_47	AP17	R8	VSS_153	VSS_206	V19	F3	VSS_47	VSS_100	K9	Y43	VSS_259	VSS_312	AG31
AF4	AVSS_48	AP23	R9	VSS_154	VSS_207	V24	F9	VSS_48	VSS_101	K9	Y44	VSS_260	VSS_313	AK28
AH8	AVSS_49	AP34	R11	VSS_155	VSS_208	V25	F10	VSS_49	VSS_102	K18	Y45	VSS_261	VSS_314	AK29
AVSS_50	AVSS_101		R13	VSS_156	VSS_209	V27	F11	VSS_50	VSS_103	K21	Y46	VSS_262	VSS_315	AL25
AVSS_51			R15	VSS_157	VSS_210	V30	F13	VSS_51	VSS_104	K22	Y47	VSS_263	VSS_316	AM30
			R18	VSS_158	VSS_211	W2	F14	VSS_52	VSS_105	L1	Y48	VSS_264	VSS_317	
				VSS_159	VSS_212			VSS_53	VSS_106			VSS_265		

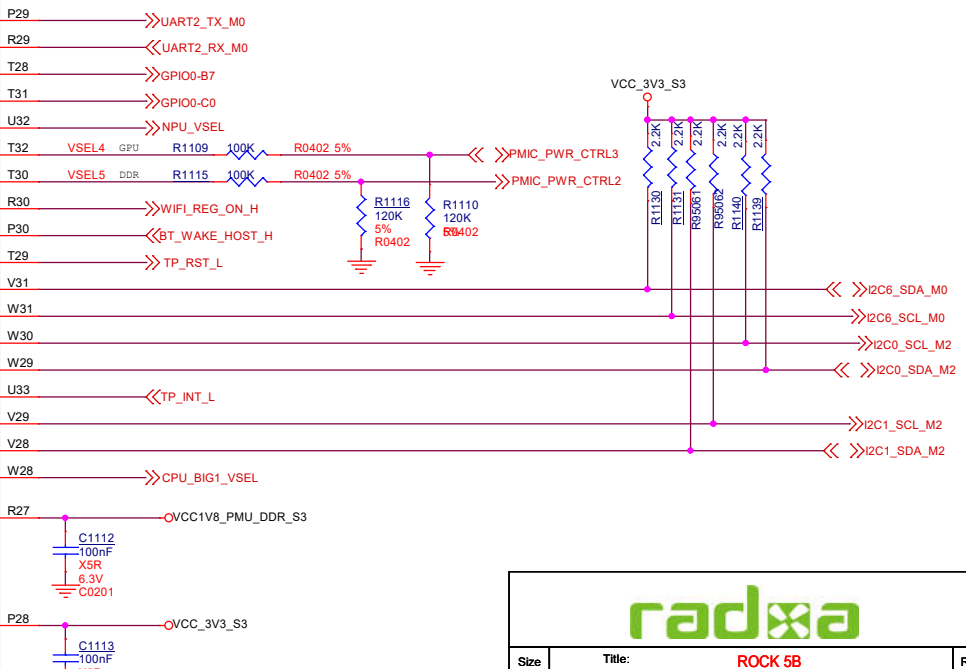
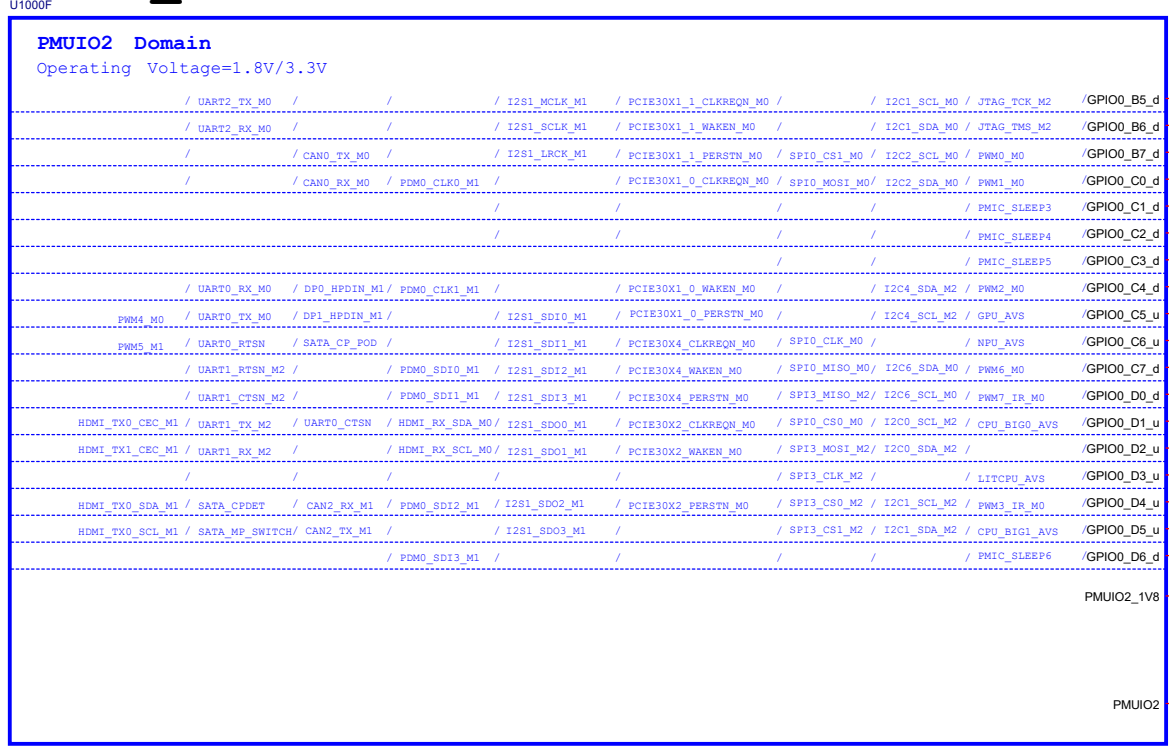
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Custom Page Name: 10.RK3588_Power/GND **1.42**
Date: Tuesday, September 20, 2022 **Sheet** 10 **of** 32

RK3588_E (OSC/PLL/PMUIO1/2)

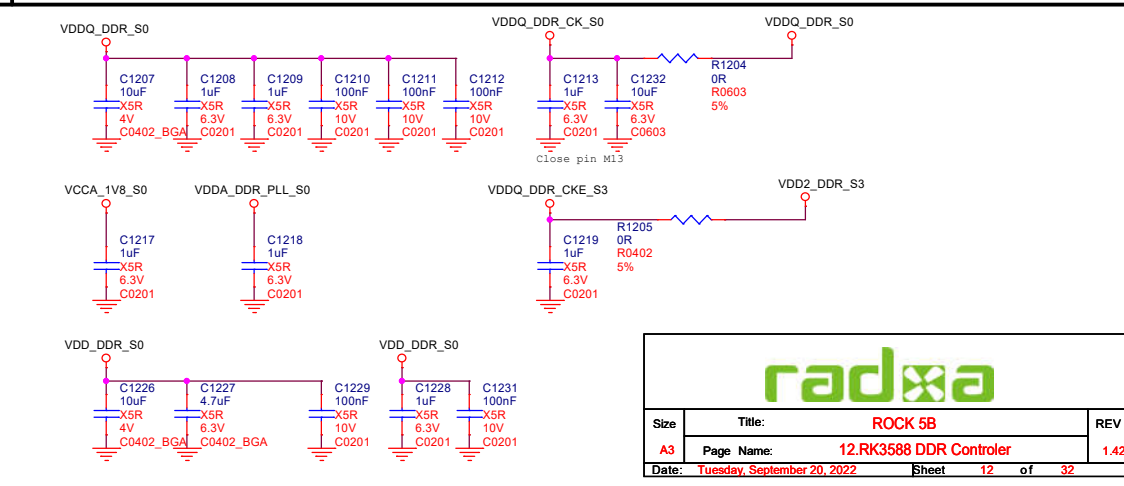
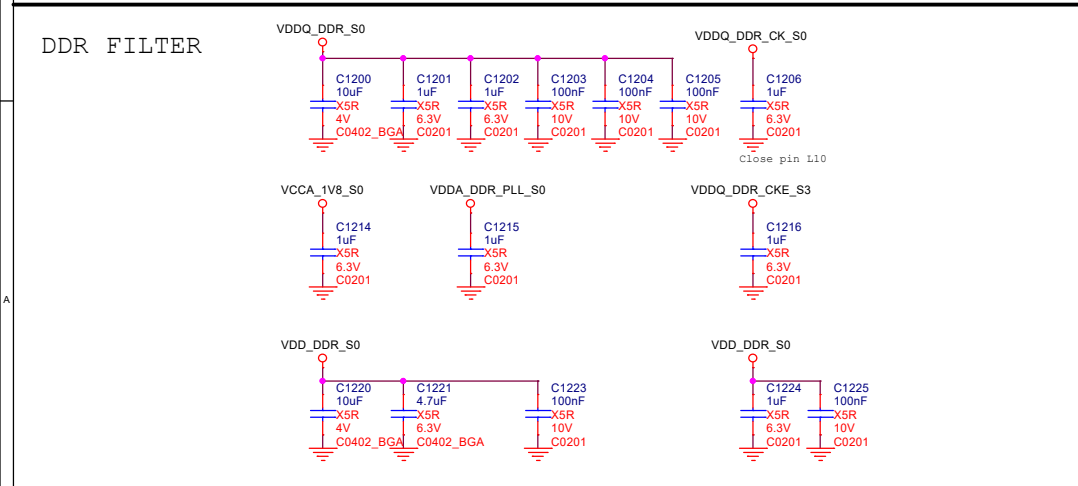
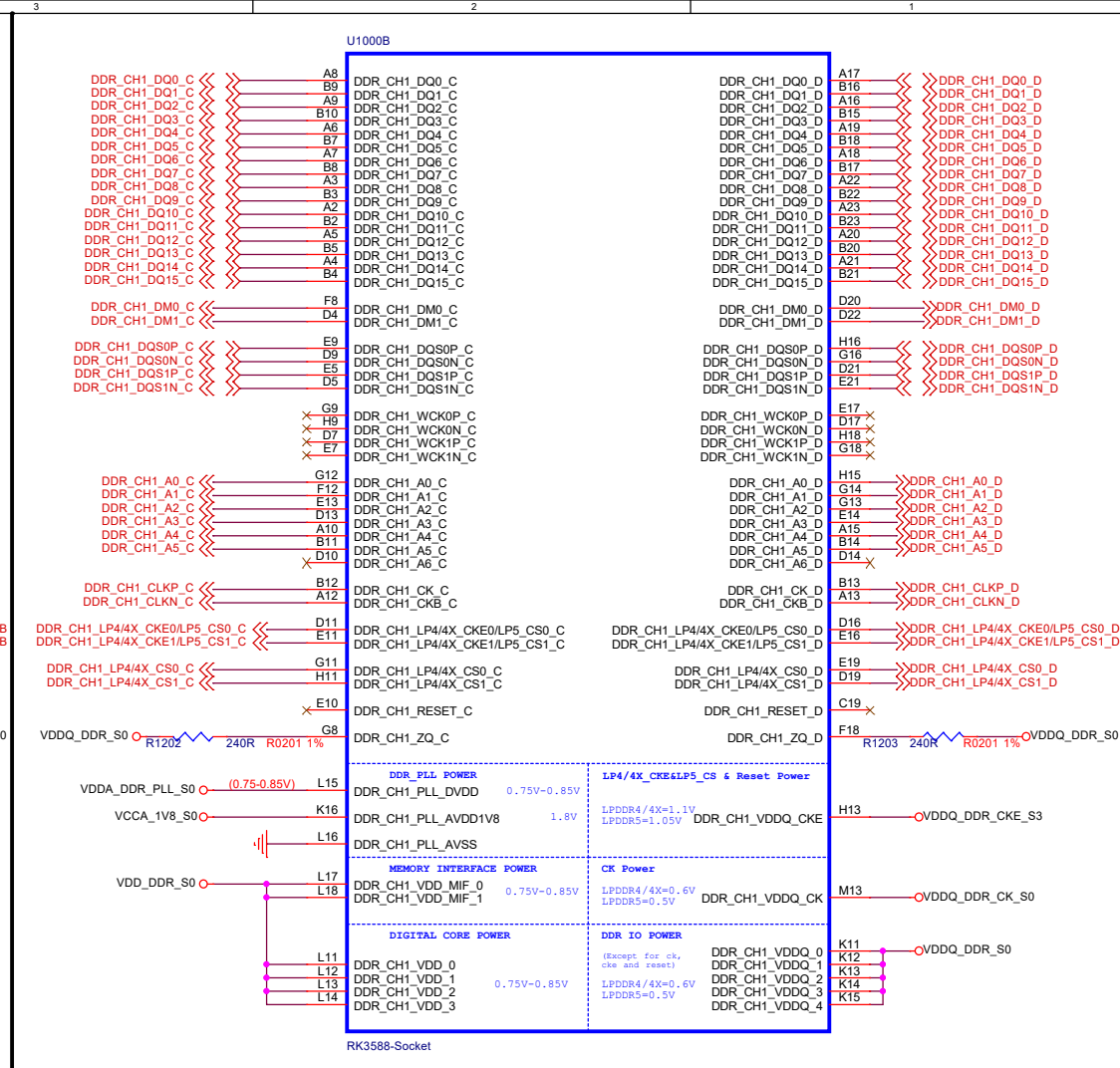
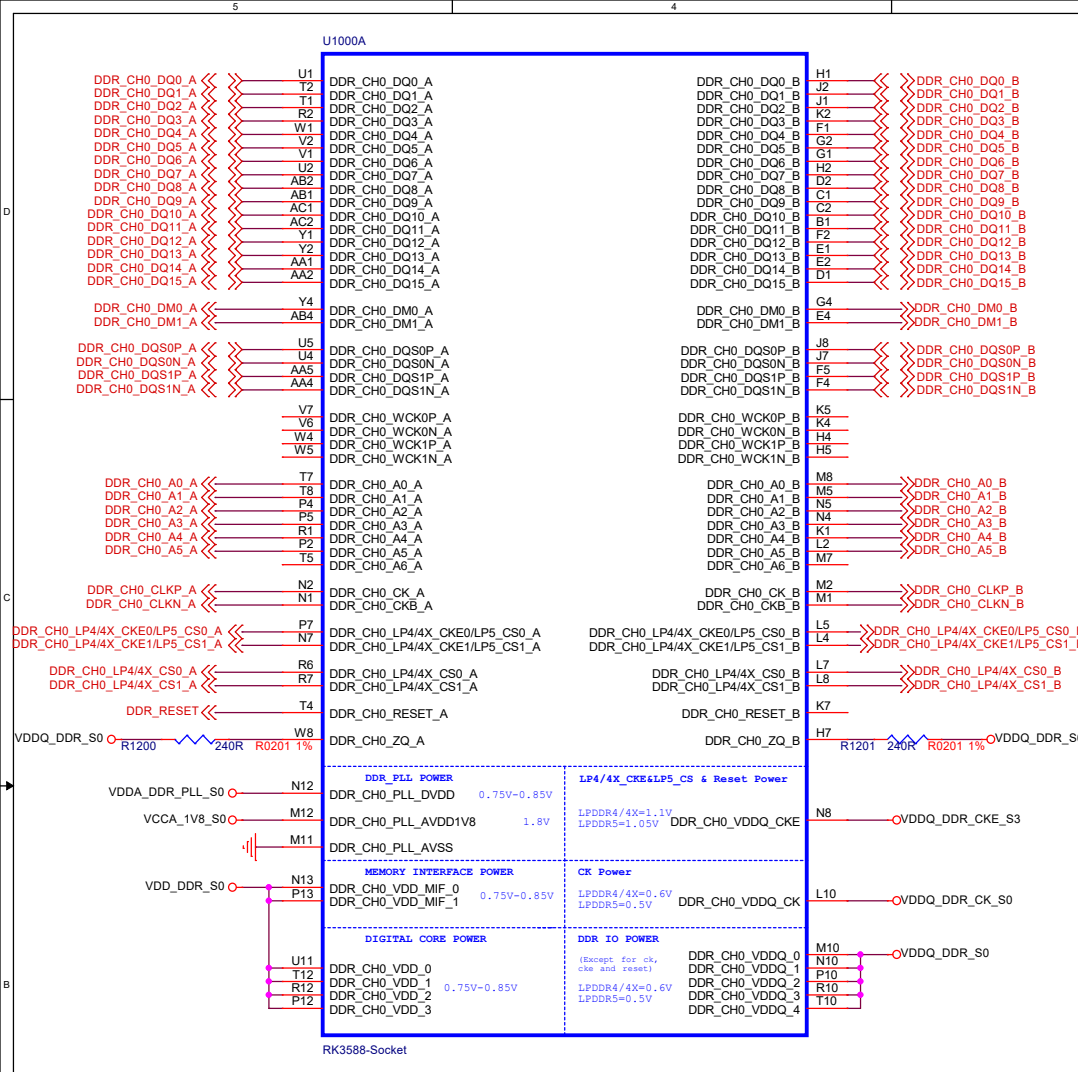
Note:
 The CL is the load capacitance of the crystal that is recommended by the crystal vendors to obtain target clock frequency.
 $CL = (CL1 + CL2) / (CL1 + CL2) + PCB \text{ strays}$
 Total $CL < 12pF$



RK3588_F (PMUIO2)



Size	Title:	ROCK 5B	REV
A3	Page Name:	11.RK3588_OSC/PLL/PMUIO	1.42
Date:	Tuesday, September 20, 2022	Sheet	11 of 32



radxa

ROCK 5B

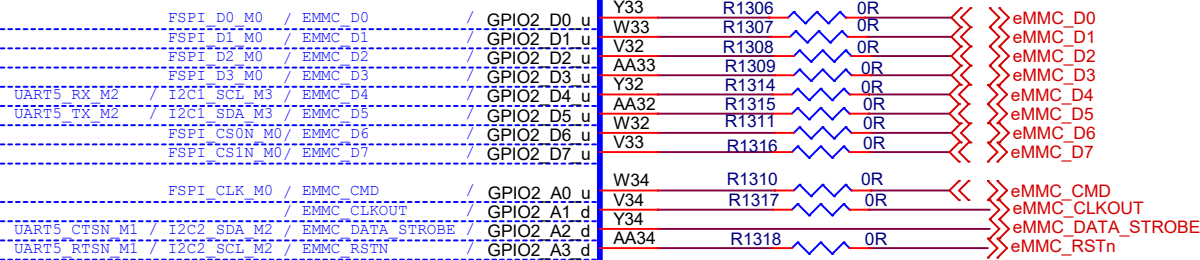
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 Page Name: 12.RK3588 DDR Controller
 Date: Tuesday, September 20, 2022 Sheet 12 of 32

RK3588_C (EMMCIO Domain)

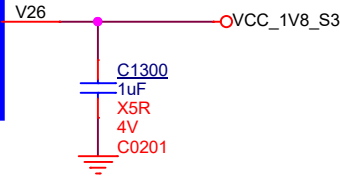
U1000C

EMMCIO Domain

Operating Voltage=1.8V



EMMCIO_1V8



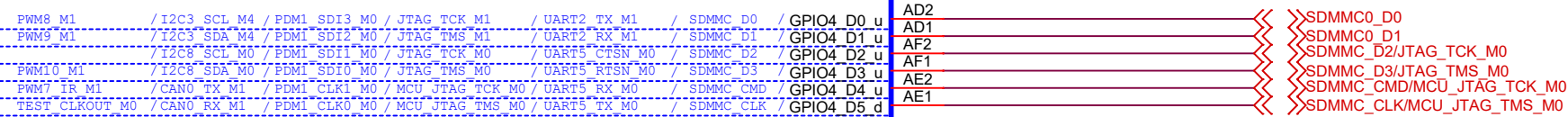
RK3588-Socket

RK3588_D (VCCIO2 Domain)

U1000D

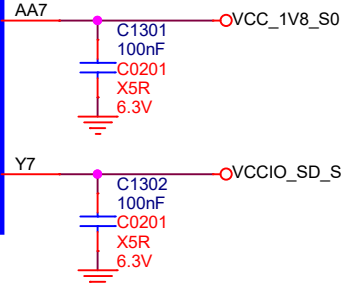
VCCIO2 Domain

Operating Voltage=1.8V/3.3V



VCCIO2_1V8

VCCIO2



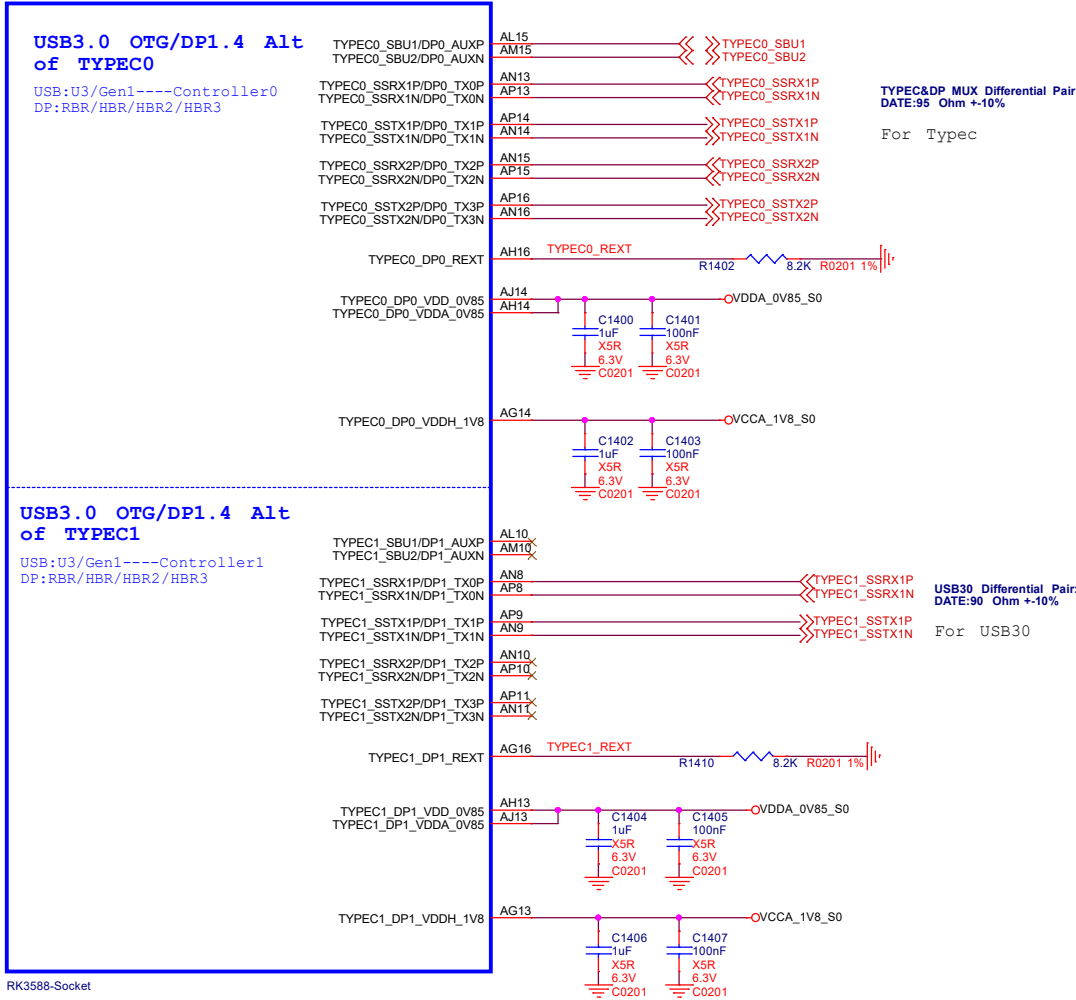
RK3588-Socket



Size	Title: ROCK 5B	REV
A4	Page Name: 13.RK3588_Flash/SD Controller	1.42
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RK3588_M (TYPEC/DP)

U1000M

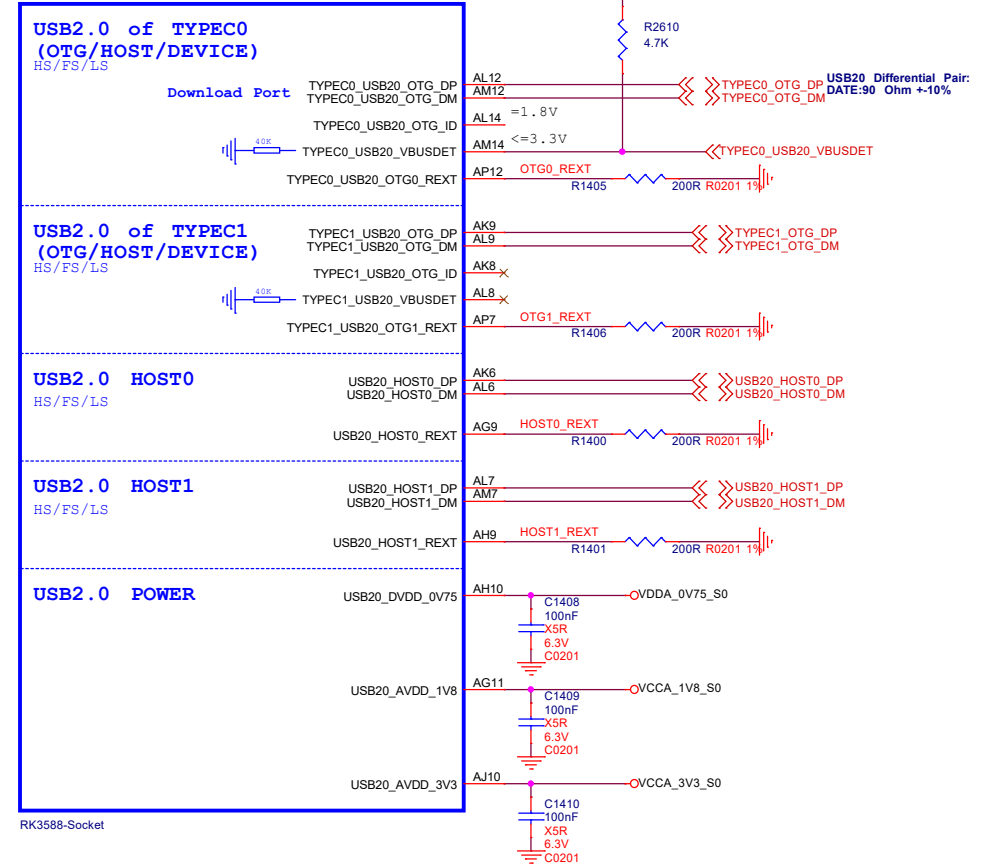


USB30/DP1.4 Alt Mode Configuration

Option1	DP x4Lane	DP_TX_Lane0-3
Option2	USB30 x4Lane	DP_TX_Lane0-3
Option3	USB30X2Lane+DPX2Lane	USB30: Lane0 Lane1 DP: Lane2 Lane3
Option4	USB30X2Lane+DPX2Lane	USB30: Lane2 Lane3 DP: Lane0 Lane1

RK3588_L (USB2.0 HOST/OTG)

U1000L

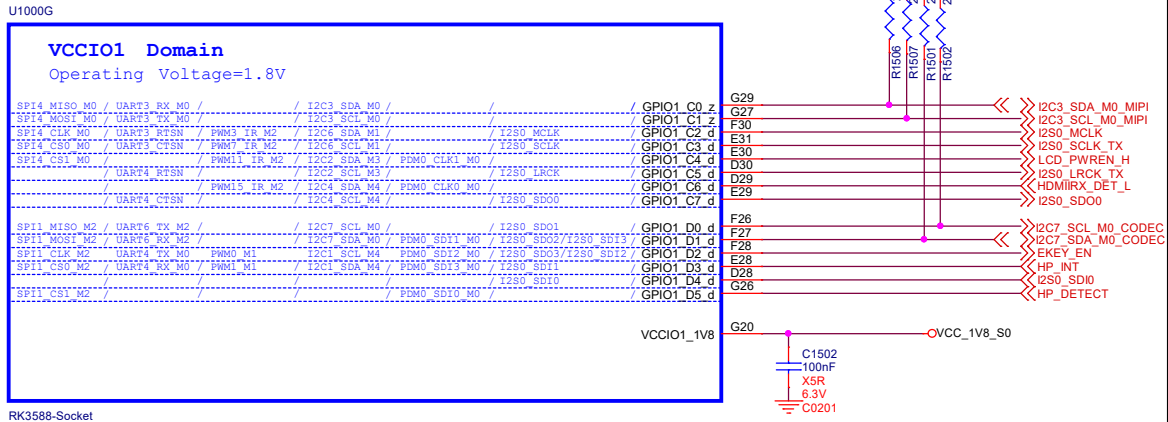


Note:
The USB20 VBUSDET pin internal has a pull-down resistance(40K ohm) to ground,The resistance creates a voltage with the external series 30K ohm resistor.The VBUSDETPin voltage range <=3.3V.

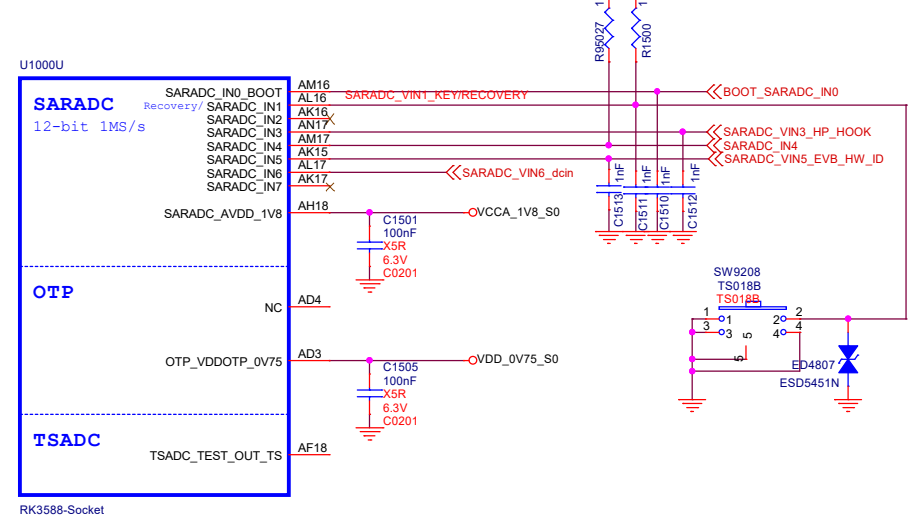


Size	Title: ROCK 5B	REV
A3	Page Name: 14.RK3588_USB30/USB20_Ctrl	1.42
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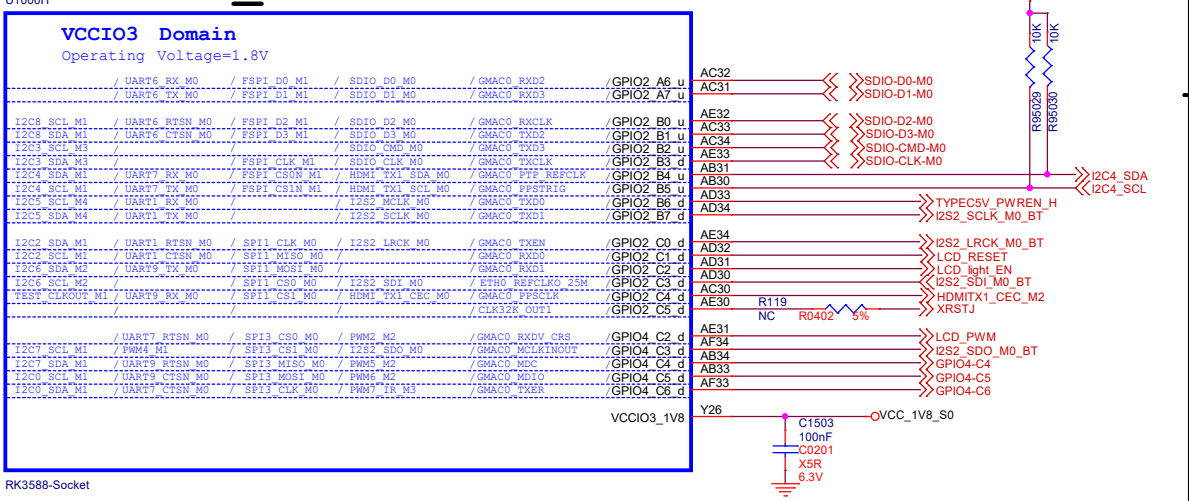
RK3588_G (VCCIO1 Domain)



RK3588_U (SARADC/OTP)



RK3588_H (VCCIO3 Domain)



BOOT MODE CONFIG

TABLE 1

Item	Rup	Rdown	ADC	VOL	BOOT MODE
LEVEL1	DNP	100K	0	0V	USB (Maskrom mode)
LEVEL2	100K	20K	682	0.3V	SD Card-USB
LEVEL3	100K	51K	1365	0.6V	EMMC-USB
LEVEL4	100K	100K	2047	0.9V	FSPI M0-USB
LEVEL5	100K	200K	2730	1.2V	FSPI M1-USB
LEVEL6	100K	499K	3412	1.5V	FSPI M2-USB
LEVEL7	100K	DNP	4095	1.8V	FSPI_M2-FSPI_M1-FSPI_M0-EMMC-SD Card-USB

BOARD ID CONFIG

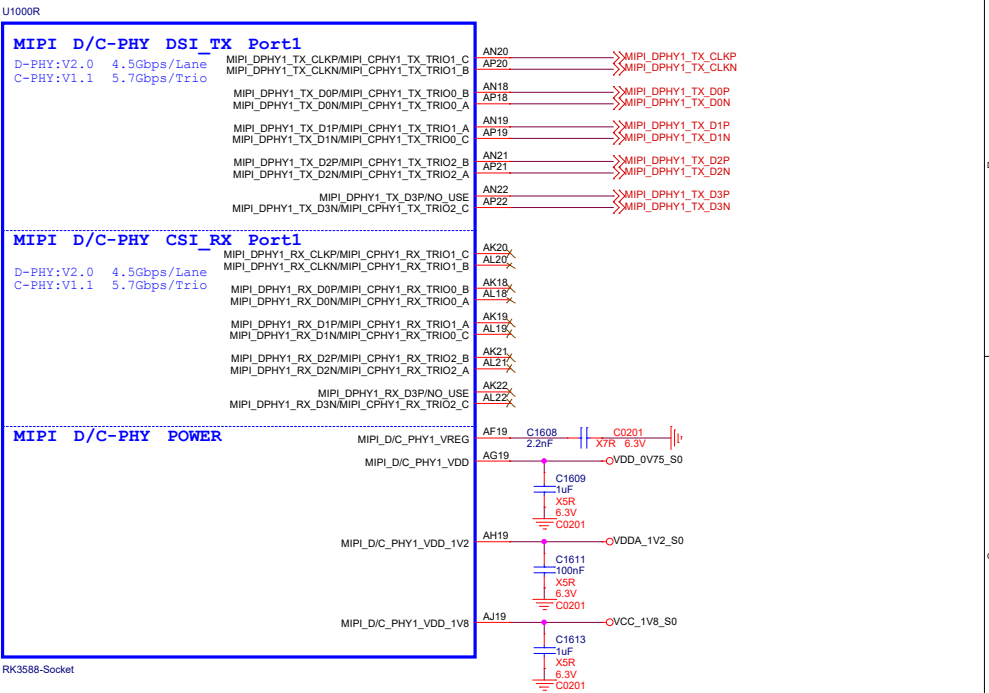
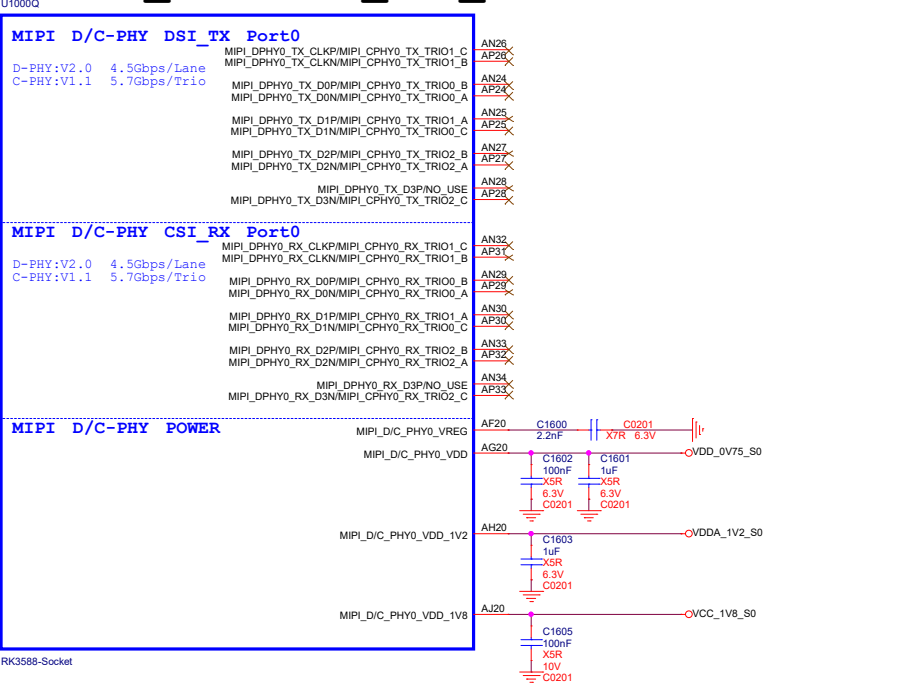
TABLE 2

Item	Rup	Rdown	ADC	VOL	VERSION
LEVEL1	DNP	100K	0	0V	A
LEVEL2	100K	20K	682	0.3V	B
LEVEL3	100K	51K	1365	0.6V	C
LEVEL4	100K	100K	2047	0.9V	D
LEVEL5	100K	200K	2730	1.2V	E
LEVEL6	100K	499K	3412	1.5V	F
LEVEL7	100K	DNP	4095	1.8V	H

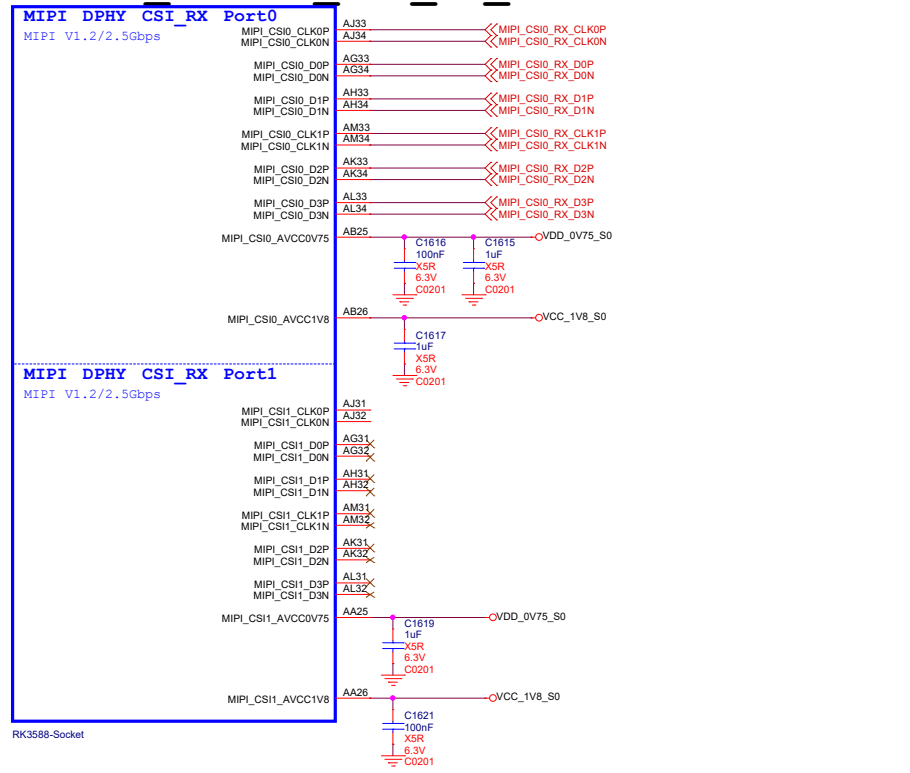

radxa

Size	Title: ROCK 5B	REV
A3	Page Name: 15.RK3588_SARADC/1.8V Only GPIO	1.42
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RK3588_Q/R (MIPI_D/C_PHY0/1)



RK3588_P (MIPI CSI_RX_PHY)

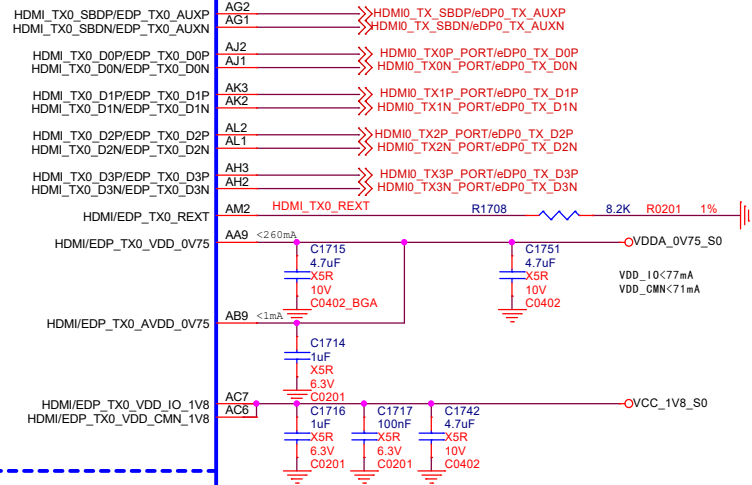
Size	Title: ROCK 5B	REV
Custom Page Name:	16.RK3588_MIPI Interface	1.42
Date:	Tuesday, September 20, 2022	Sheet 16 of 32

RK3588_S (HDMI2.1 TX)

U1000S

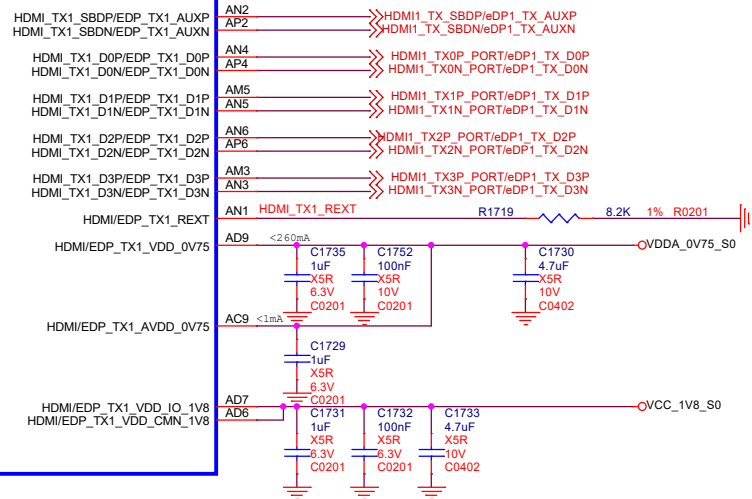
HDMI TX/eDP MUX Port0

HDMI: V2.1 12Gbps
eDP: V1.3 5.4Gbps



HDMI TX/eDP MUX Port1

HDMI: V2.1 12Gbps
eDP: V1.3 5.4Gbps



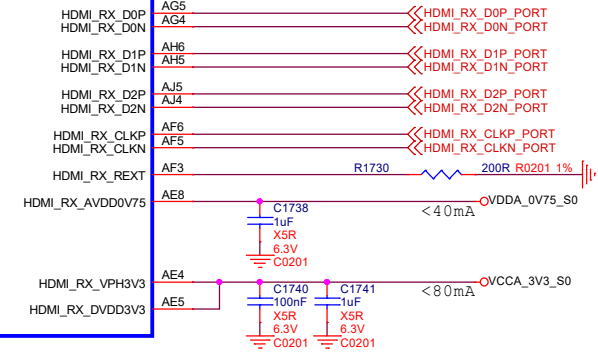
RK3588-Socket

RK3588 T (HDMI20 RX)

J1000T

HDMI RX

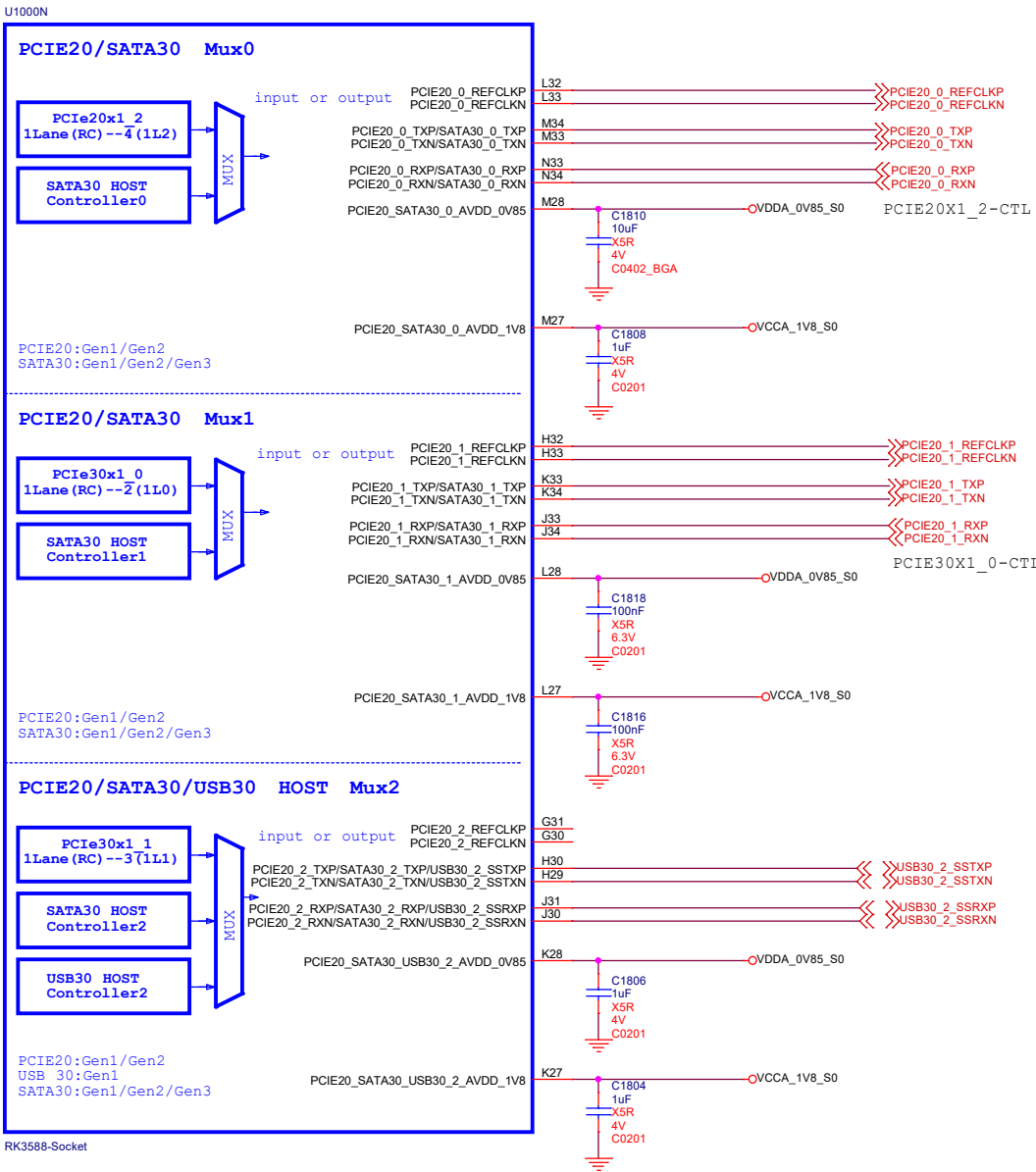
HDMI: V2.0



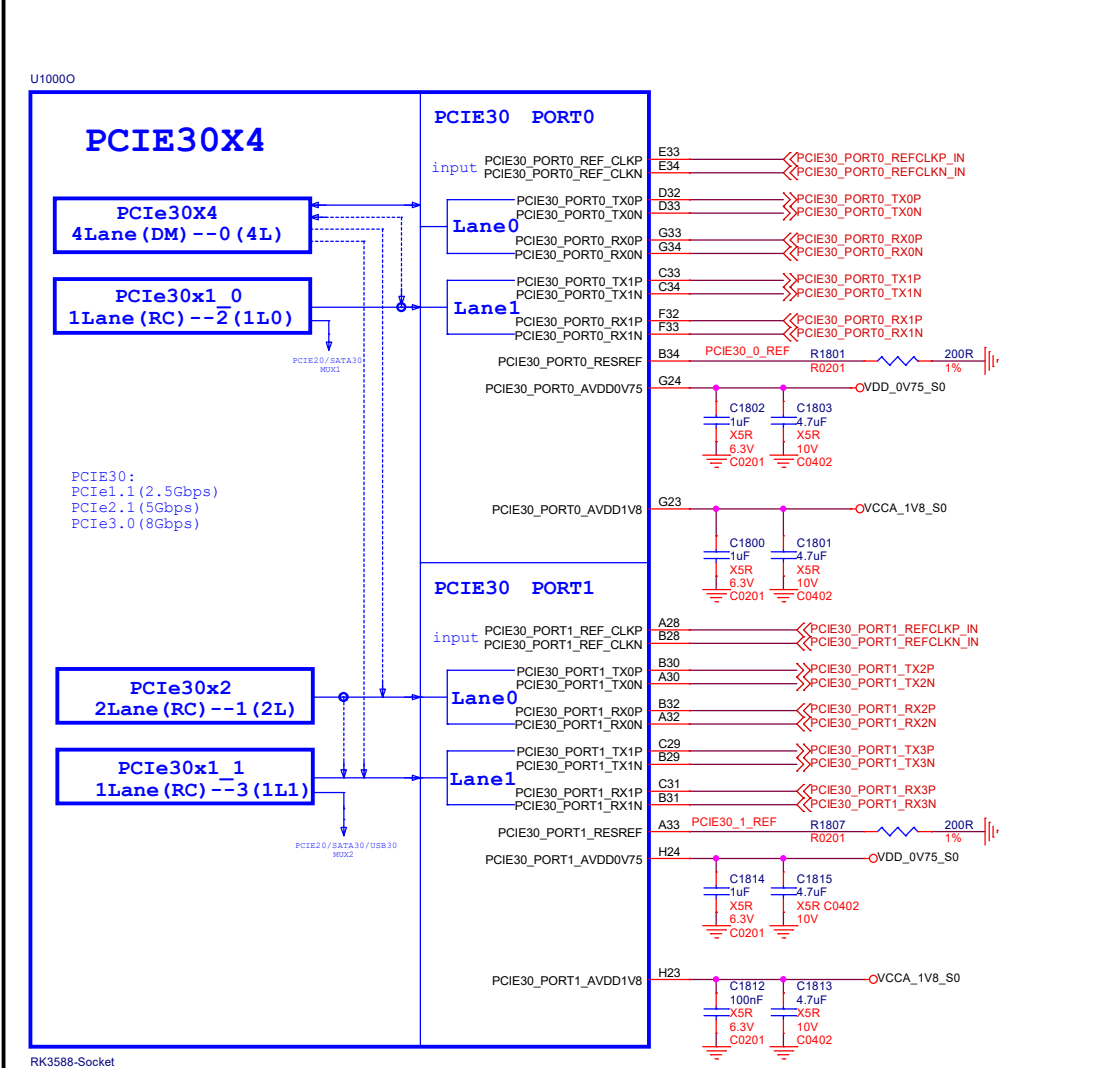
RK3588-Socket



RK3588_N (PCIE20)



RK3588_O (PCIE30)



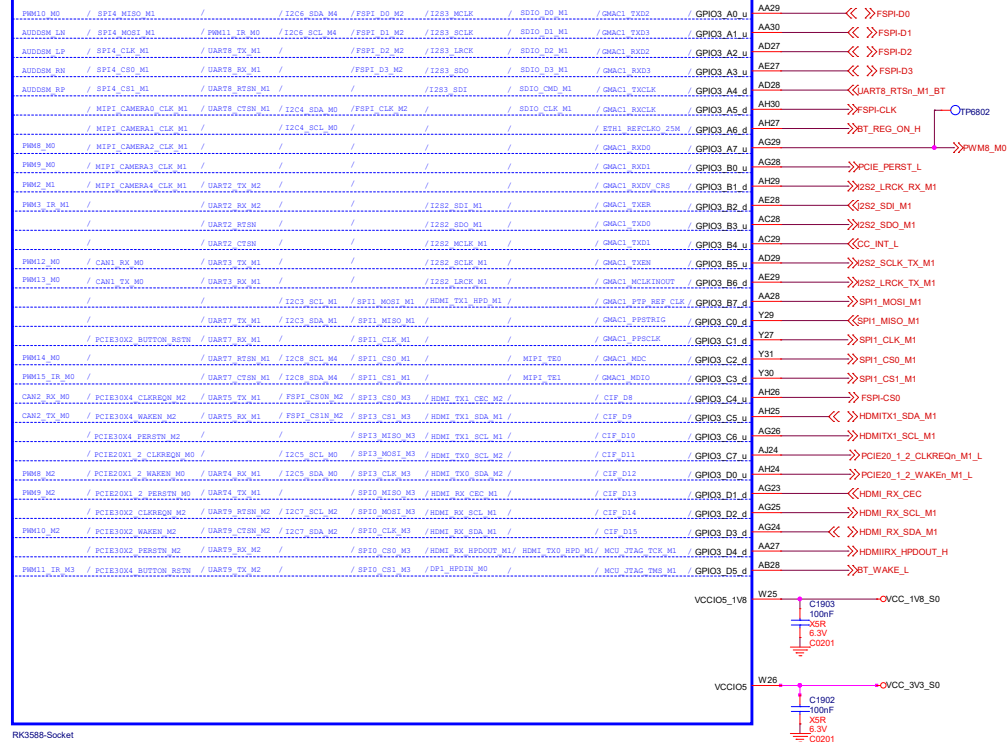
radxa

Size	Title:	ROCK 5B	REV
A3	Page Name:	18.RK3588_PCIE30/PCIE20/SATA30	1.42
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RK3588_J (VCCIO5 Domain)

U1000J

VCCIO5 Domain
Operating Voltage=1.8V/3.3V



RK3588-Socket

RK3588_K (VCCIO6 Domain)

U1000K

VCCIO6 Domain
Operating Voltage=1.8V/3.3V

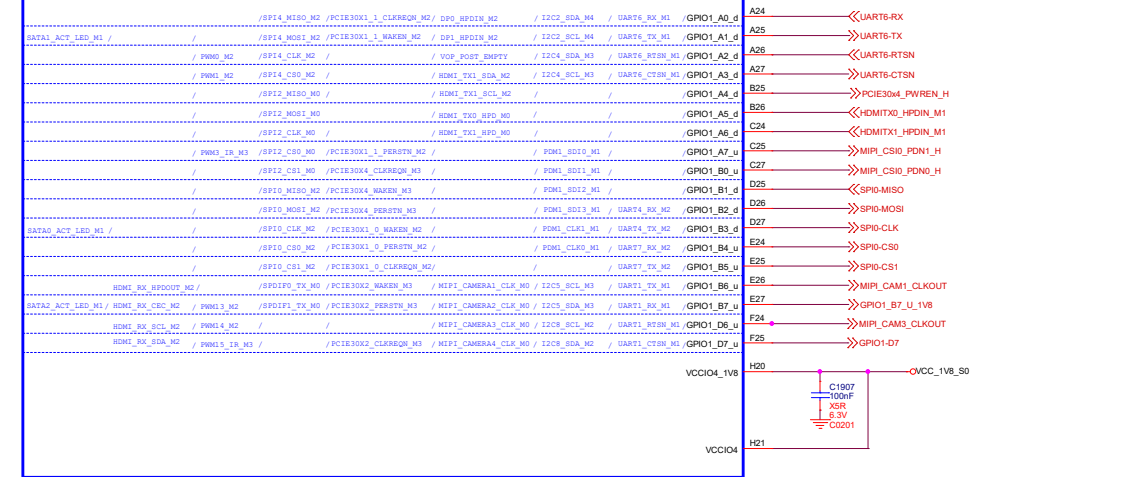


RK3588-Socket


RK3588_I (VCCIO4 Domain)

U1000I

VCCIO4 Domain
Operating Voltage=1.8V/3.3V

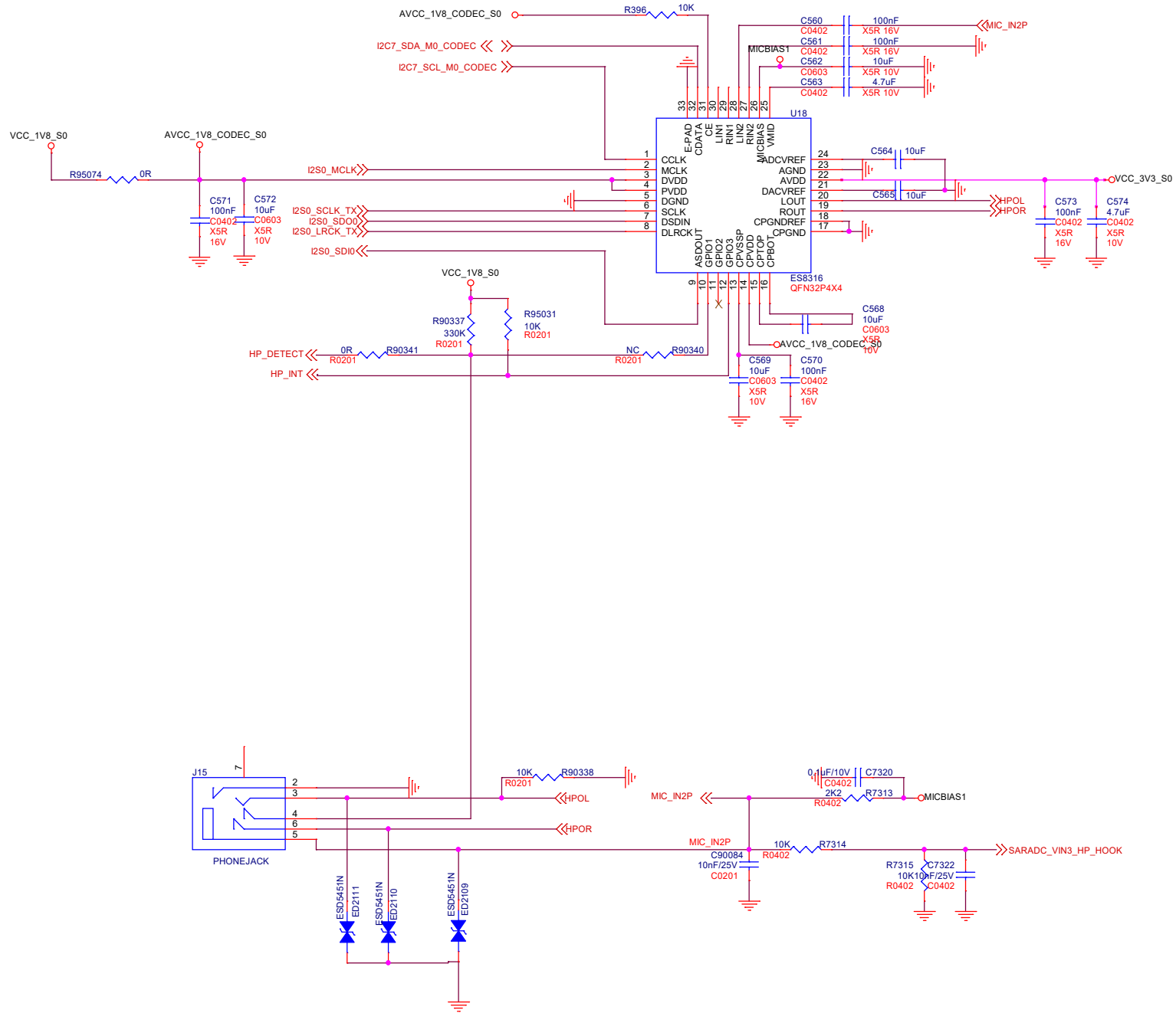


RK3588-Socket

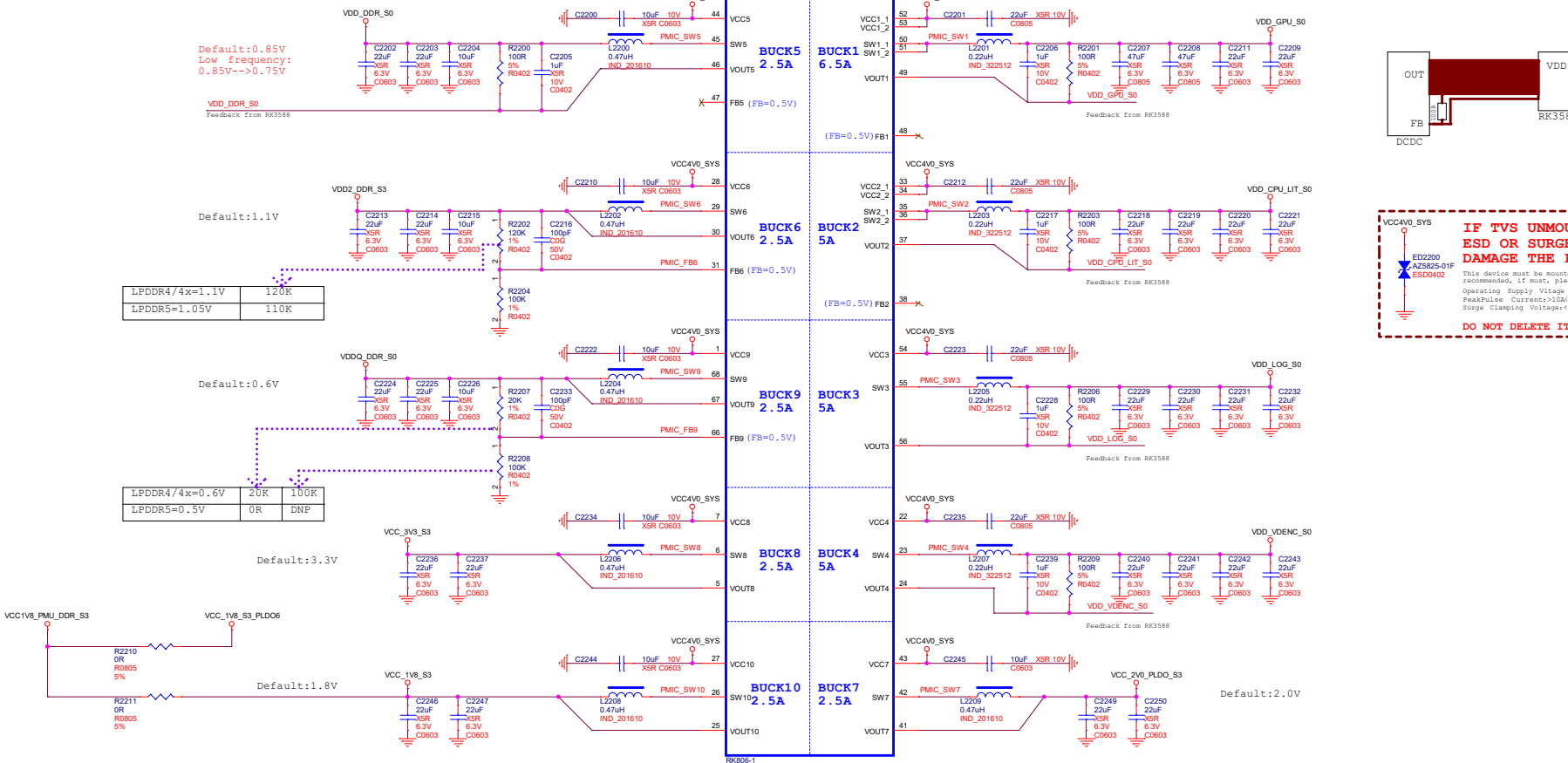


Size	Rev
A2	1.42
Title: ROCK 5B	
Page Name: 19_RK3588_1.8V/3.3V GPIO	
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CODEC ES8316

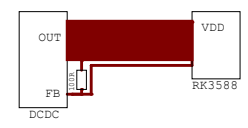


PMIC RK806-1 BUCK



LPDDR4/4x=1.1V	120K
LPDDR5=1.05V	110K

LPDDR4/4x=0.6V	20K	100K
LPDDR5=0.5V	0R	DNP

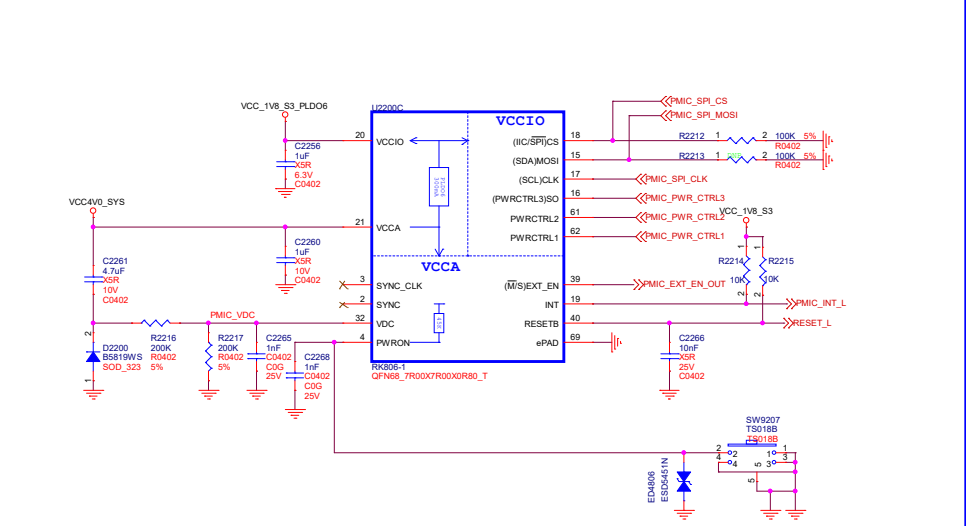


IF TVS UNMOUNTED, ESD OR SURGE SHOULD BE DAMAGE THE PMIC!!!

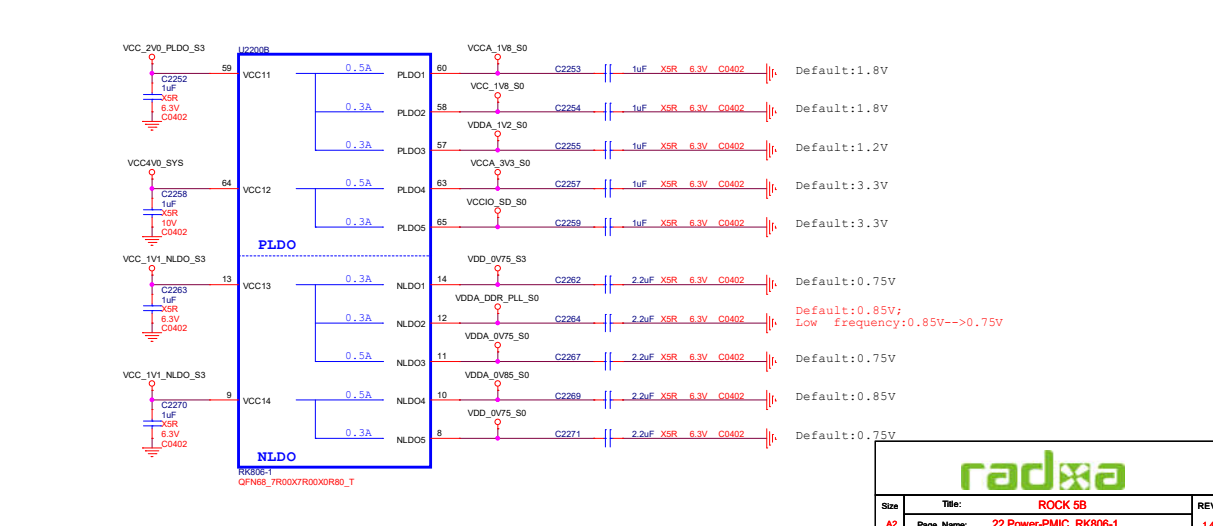
This device must be mounted. Replacing TVS mode is not recommended, if must, please choose the same specification
 Operating Supply Voltage: ±1.5V(±25%)
 PeakPulse Current: >10A(tp8/20us)
 Surge Clamping Voltage: <6.5V

DO NOT DELETE IT!

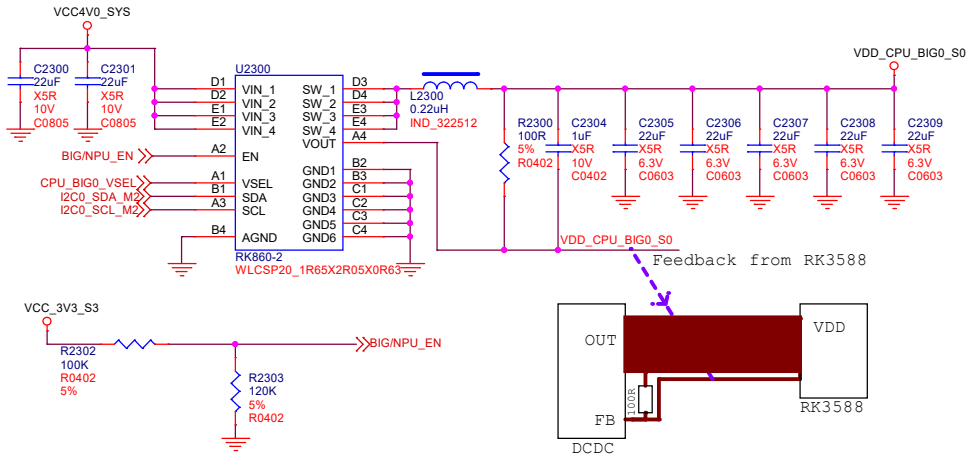
PMIC RK806-1 Management



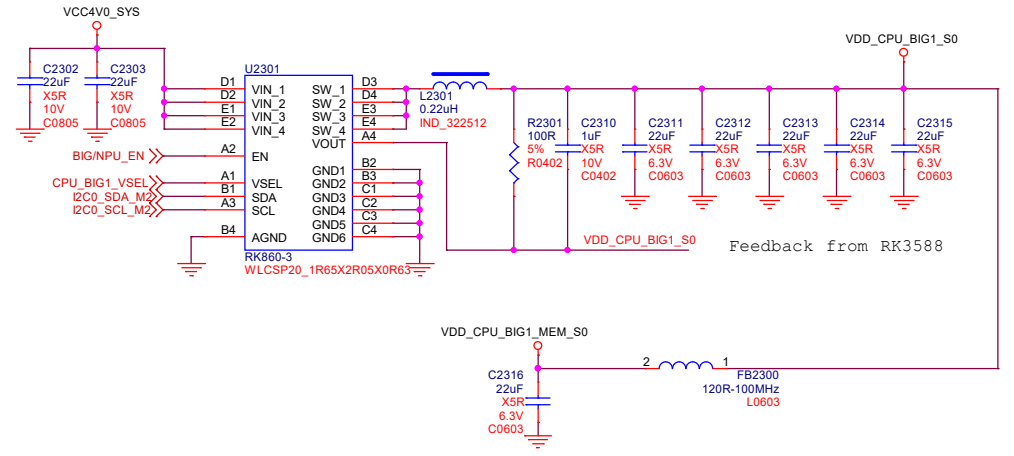
PMIC RK806-1 LDO



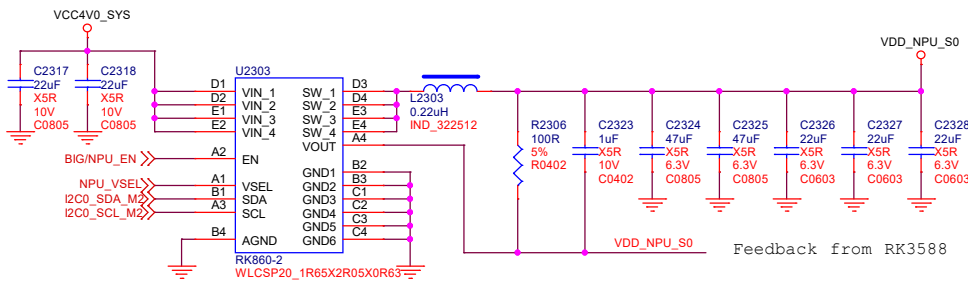
VDD_CPU_BIG0



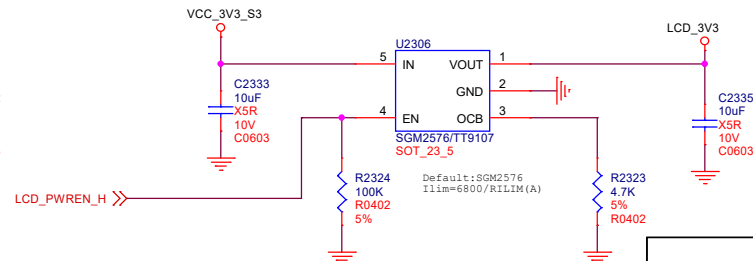
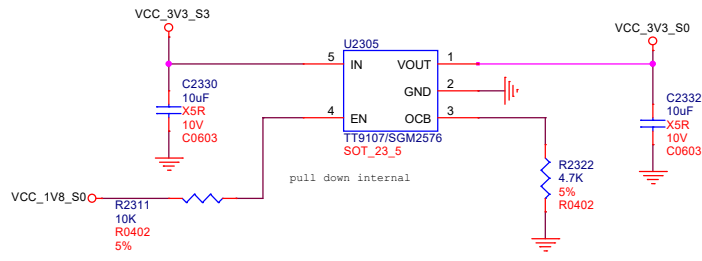
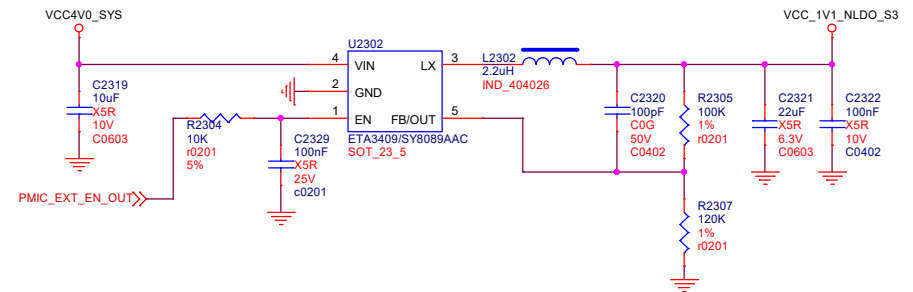
VDD_CPU_BIG1

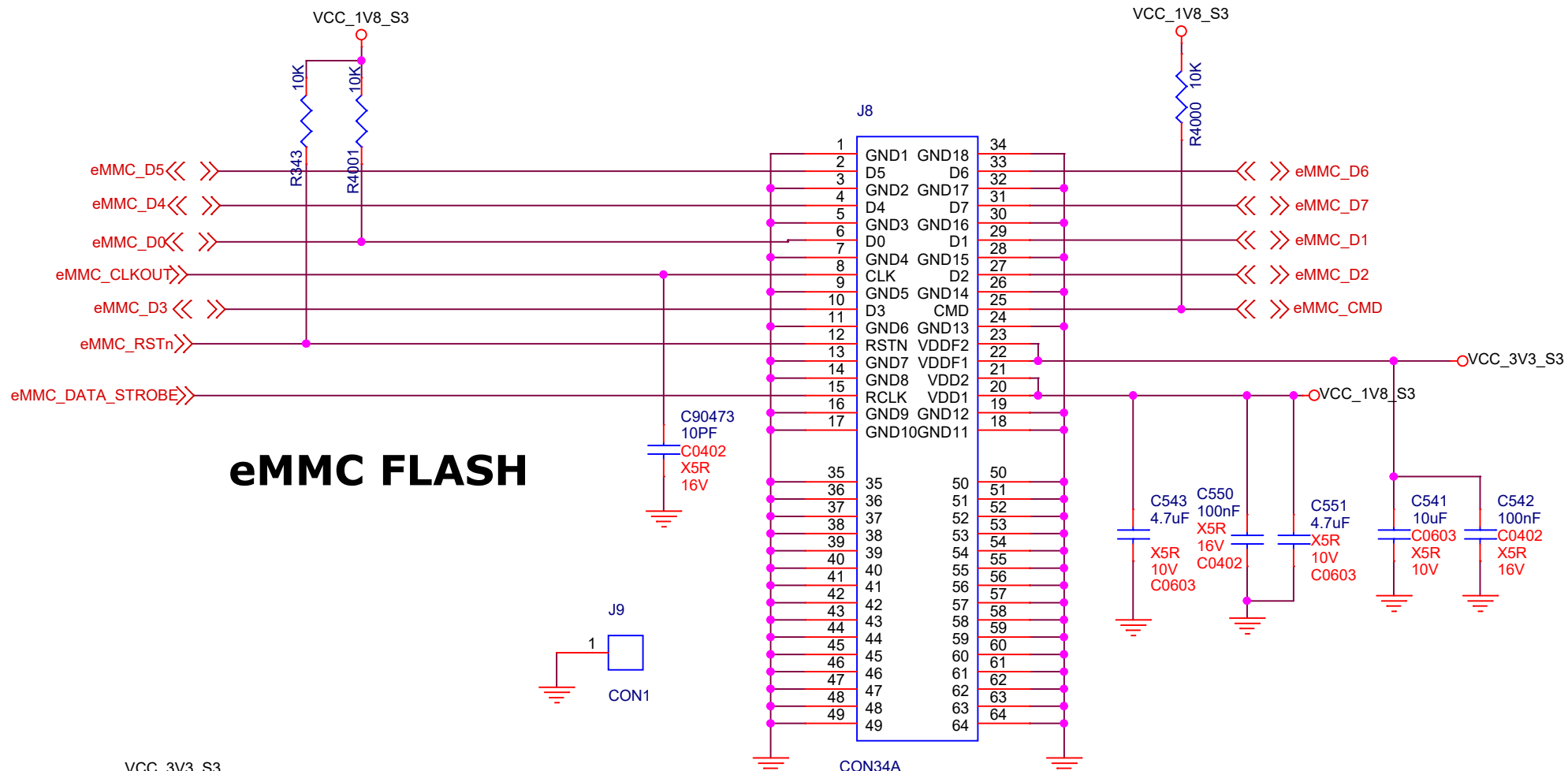


VDD_NPU

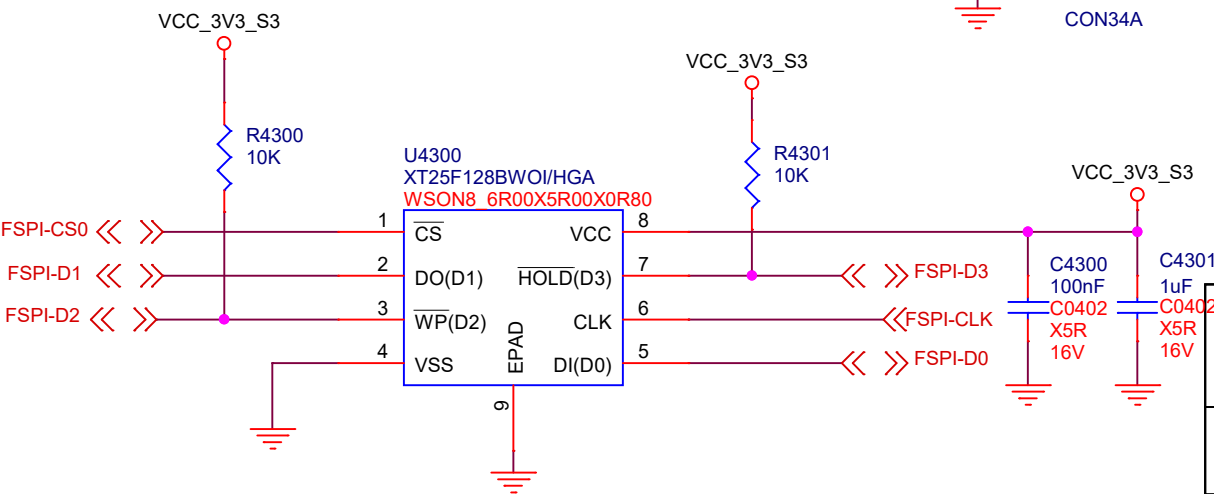


VCC_1V1_NLDO_S3



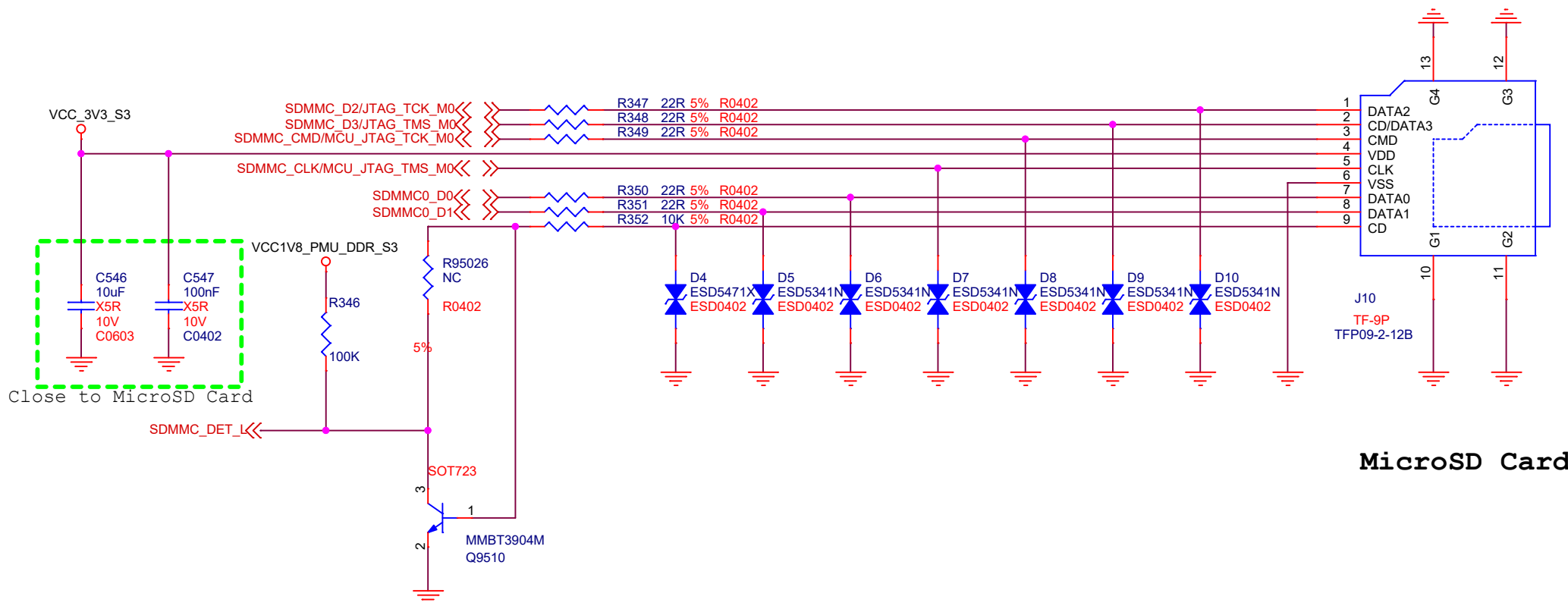


eMMC FLASH



Size	Title: ROCK 5B	REV
A	Page Name: 25.Flash-eMMC Flash	1.42
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TF CARD



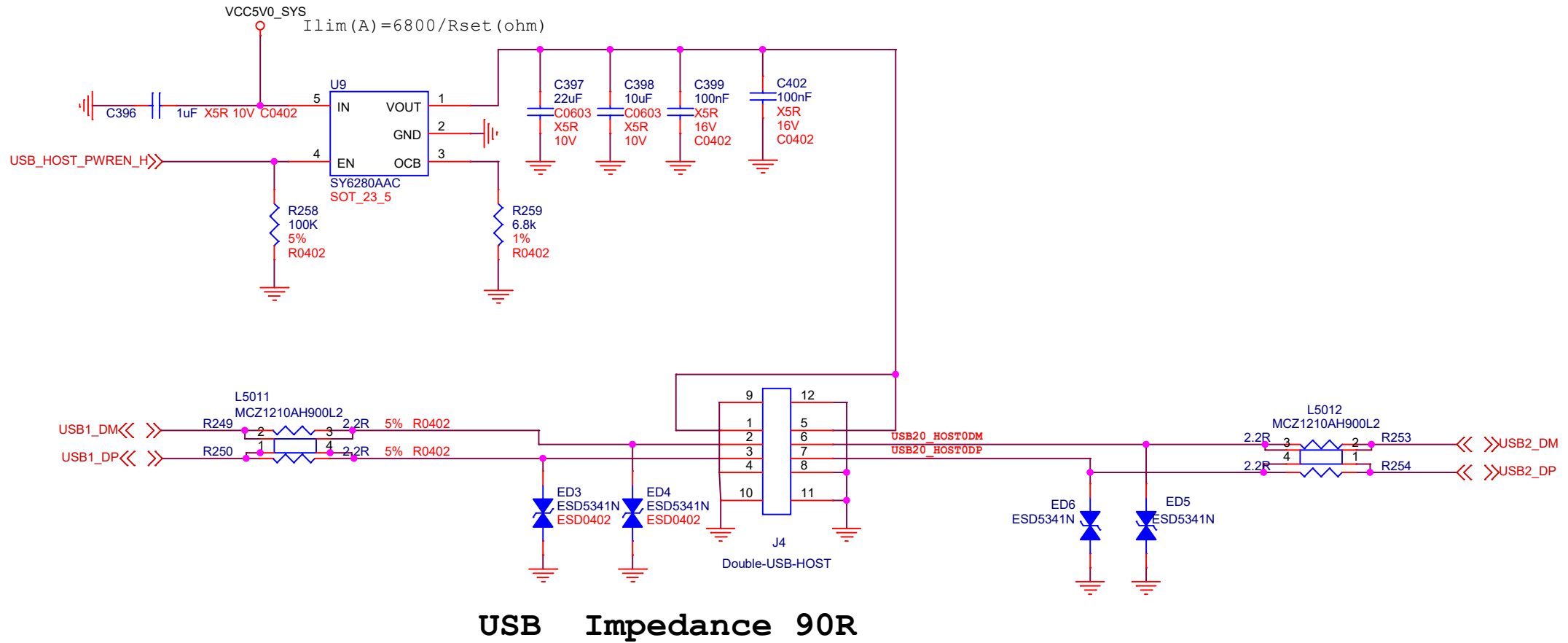
MicroSD Card



Rockchip Confidential

radxa		
Size	Title: ROCK 5B	REV
A4	Page Name: 26.Flash-TF Card	1.42
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USB2.0

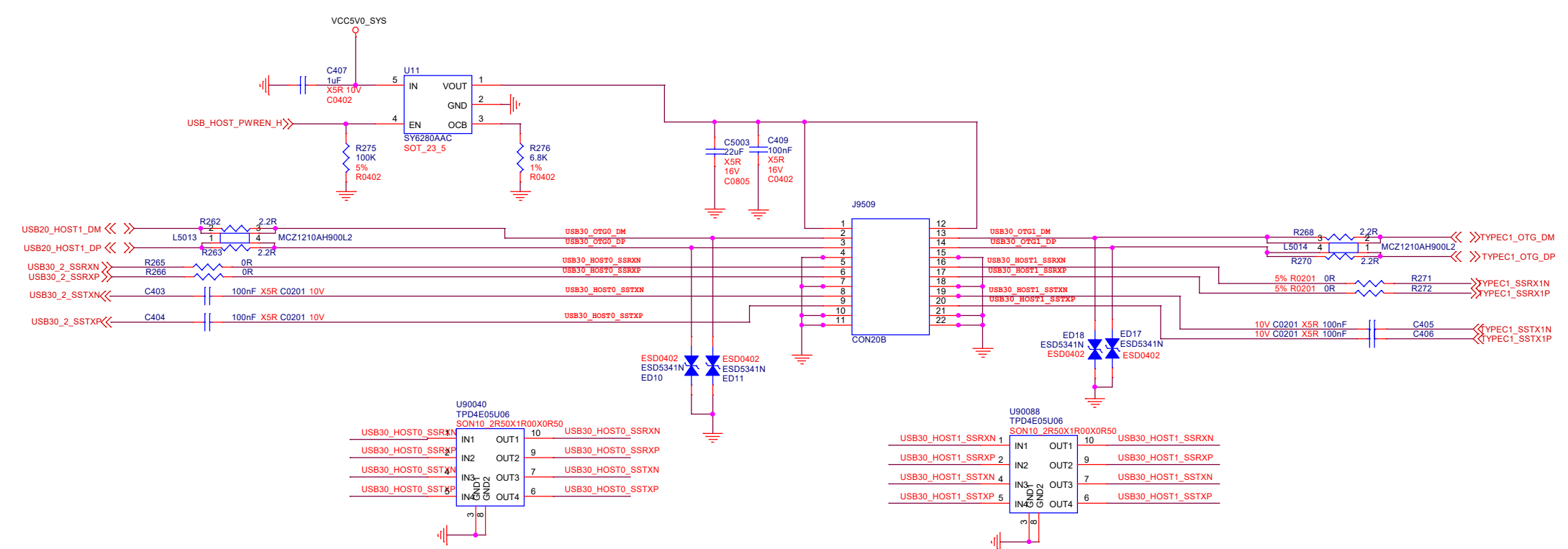



USB Impedance 90R



Size	Title: ROCK 5B	REV
A4	Page Name: 27.USB20x2 Double Port	1.42
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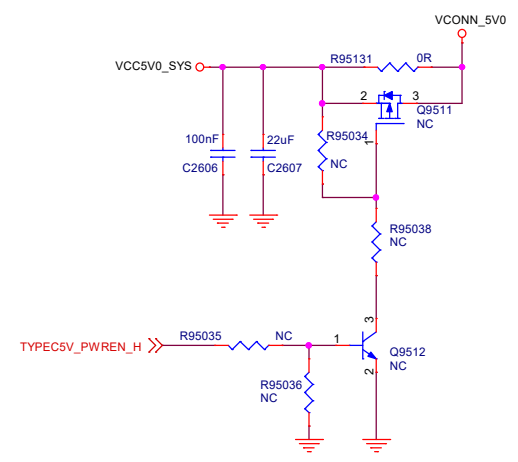
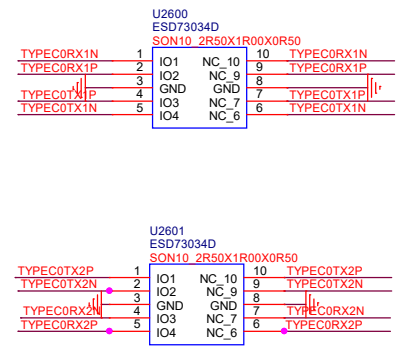
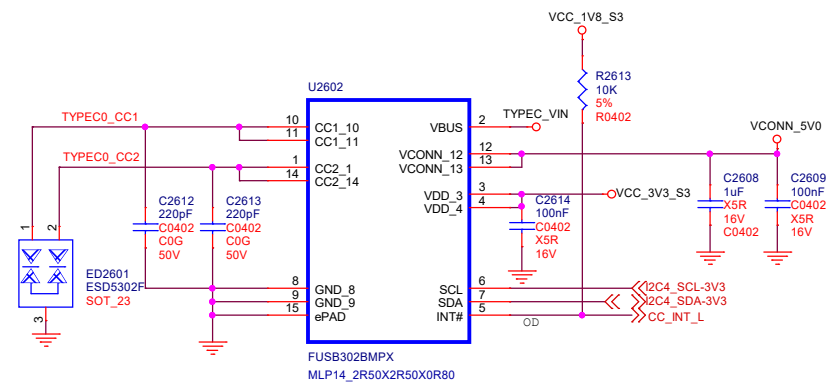
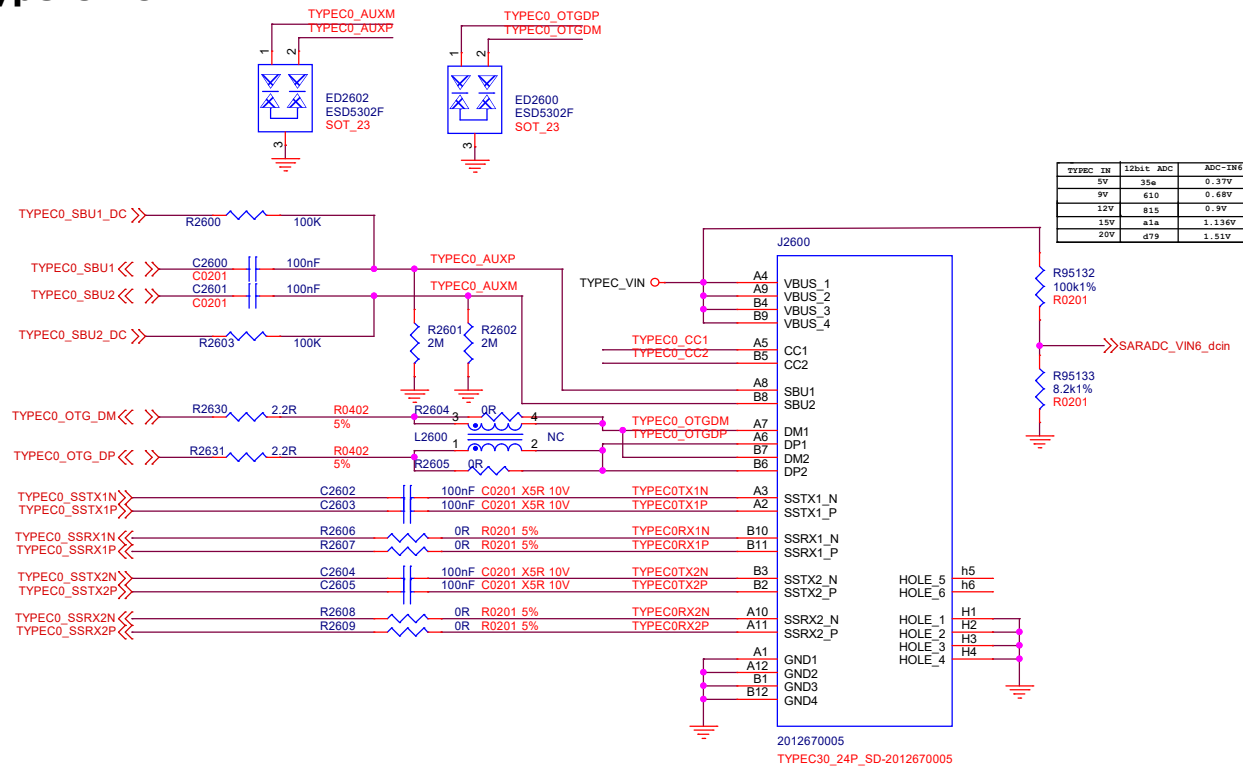
USB3.0 HOST PORT



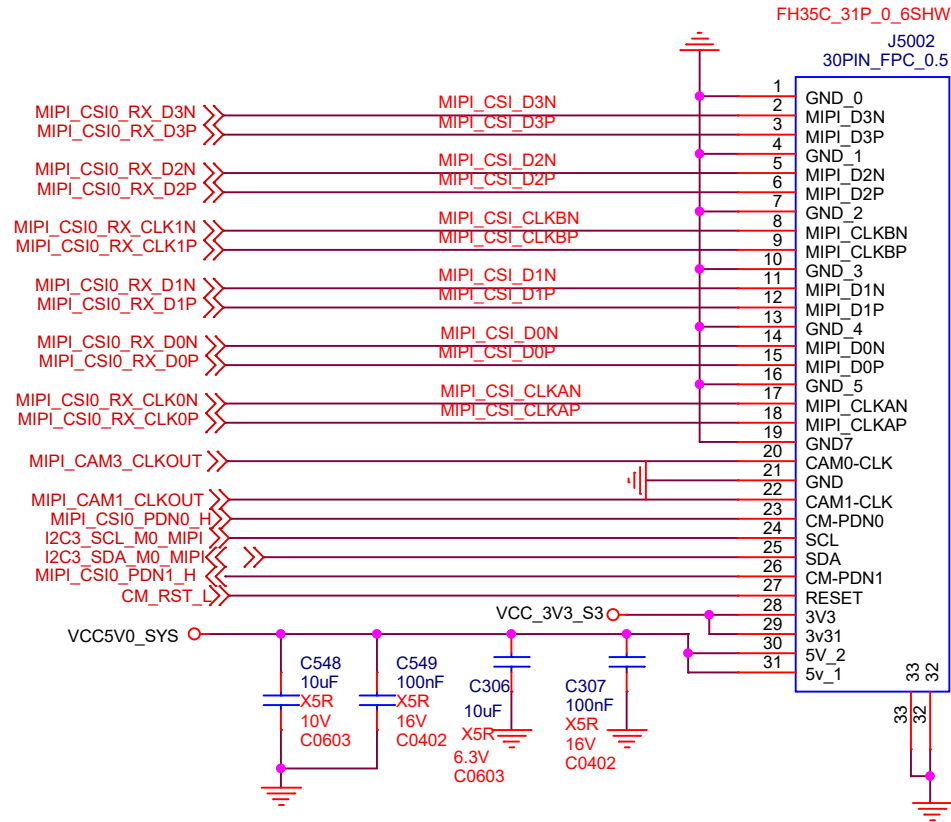


Size	Title: ROCK 5B	REV
A3	Page Name: 28.USB30x2 Double Port	1.42
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Type-C PORT

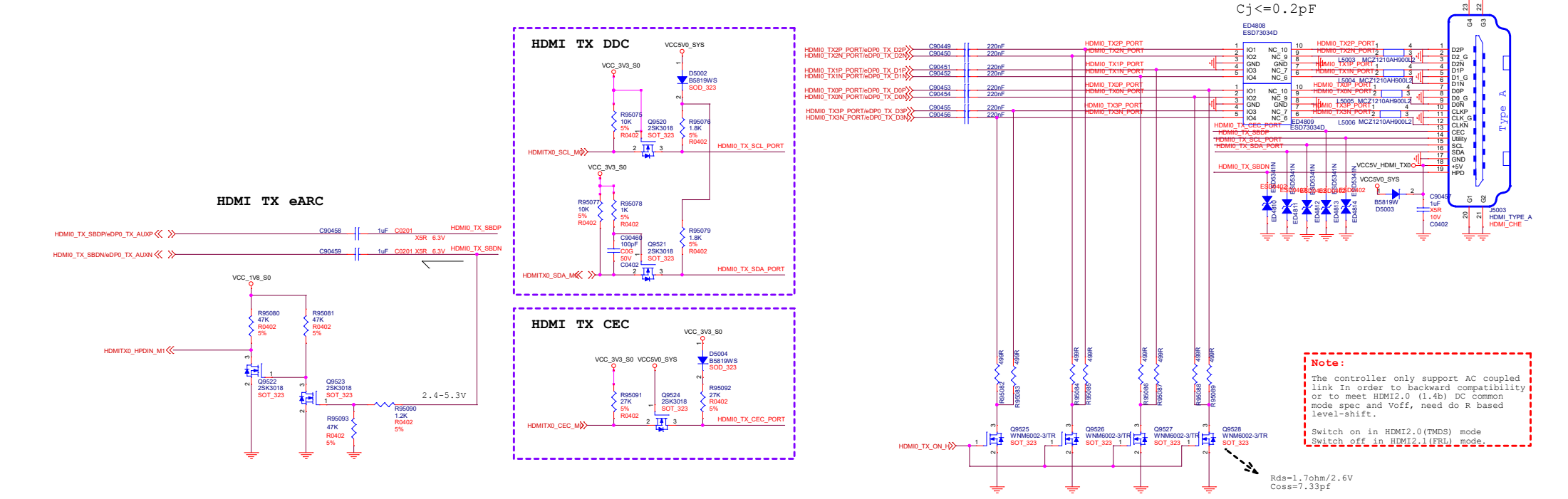


CAM



Size	Title: ROCK 5B	REV
A4	Page Name: 30.VI-Camera_MIPI-CSI	1.42
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HDMI TX0



HDMI TX1

